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FIG. 1

PRIOR ART

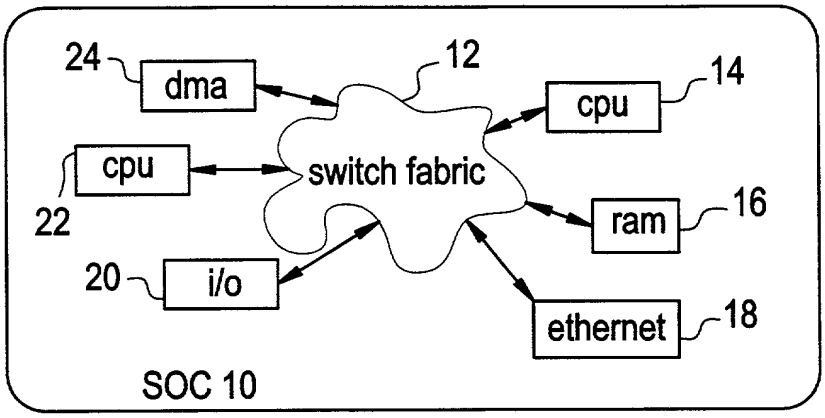
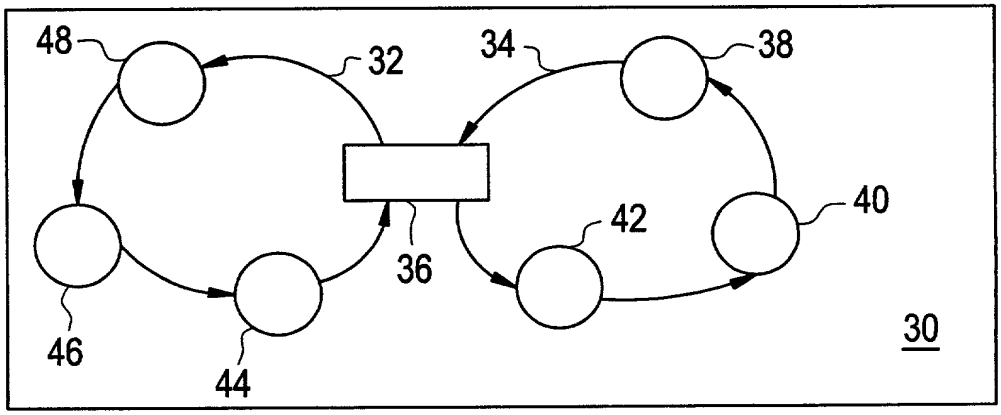


FIG. 2



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FIG. 3

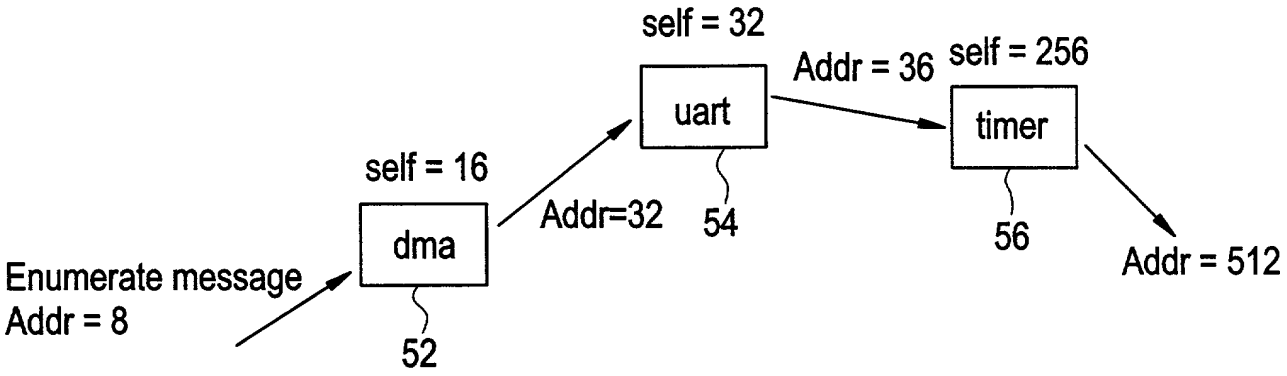


FIG. 4

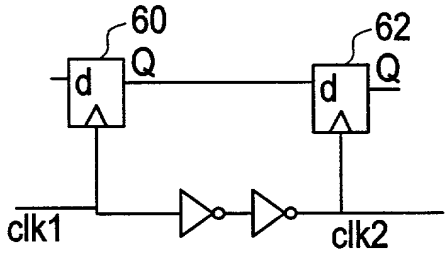
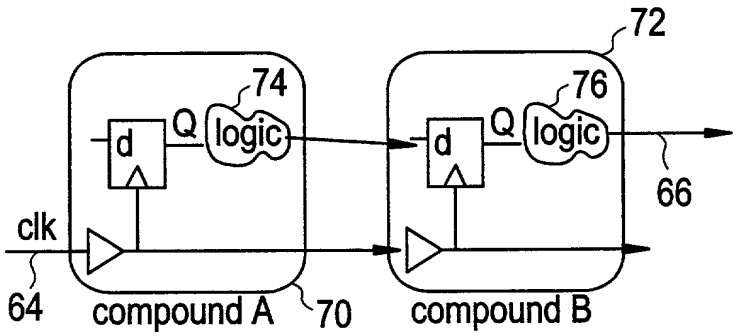


FIG. 5



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FIG. 6

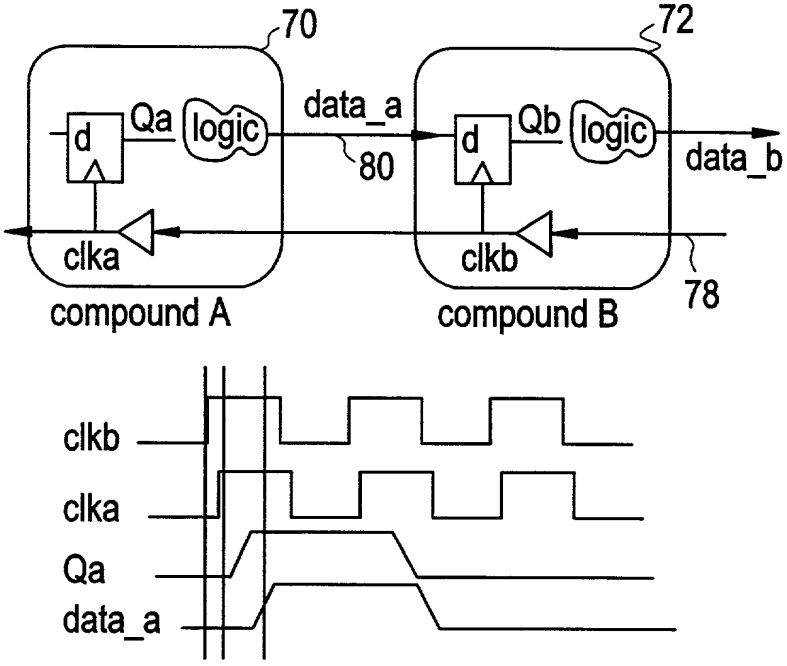
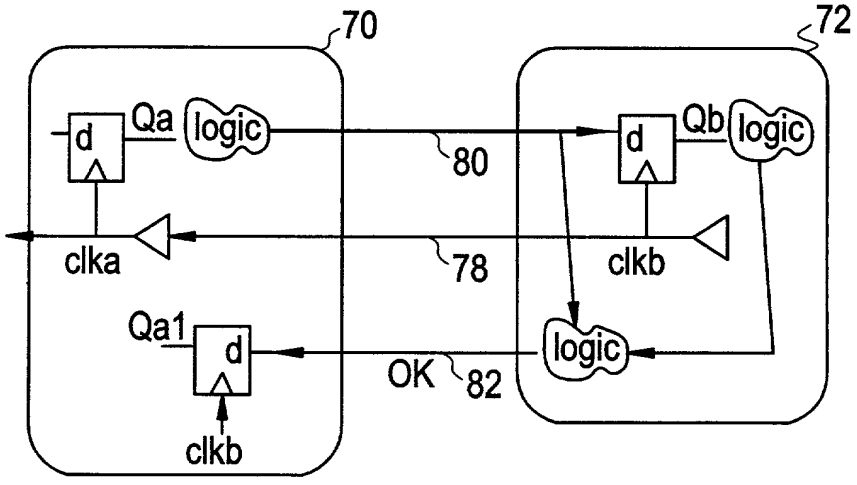


FIG. 7



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FIG. 8

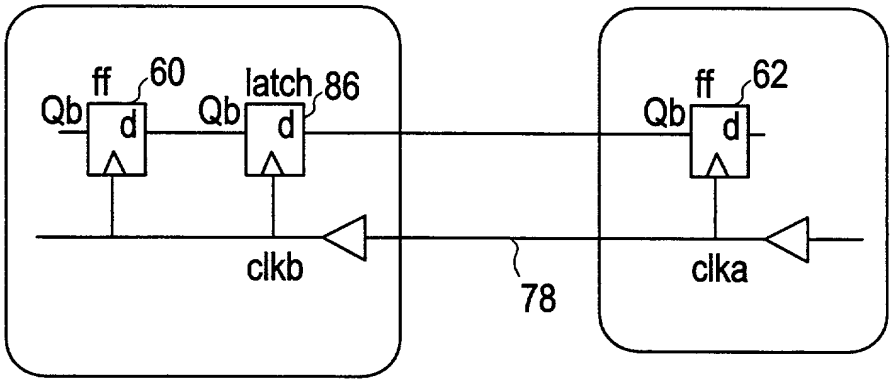
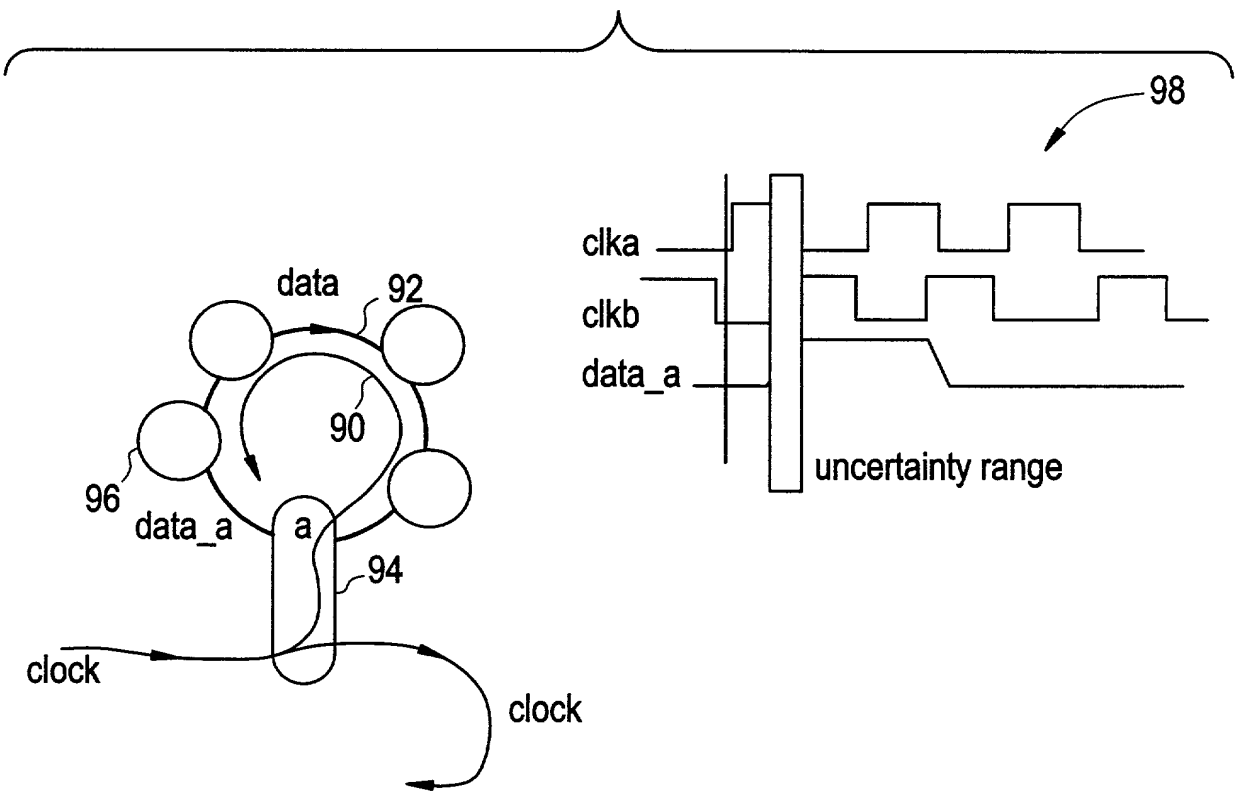


FIG. 9



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FIG. 10

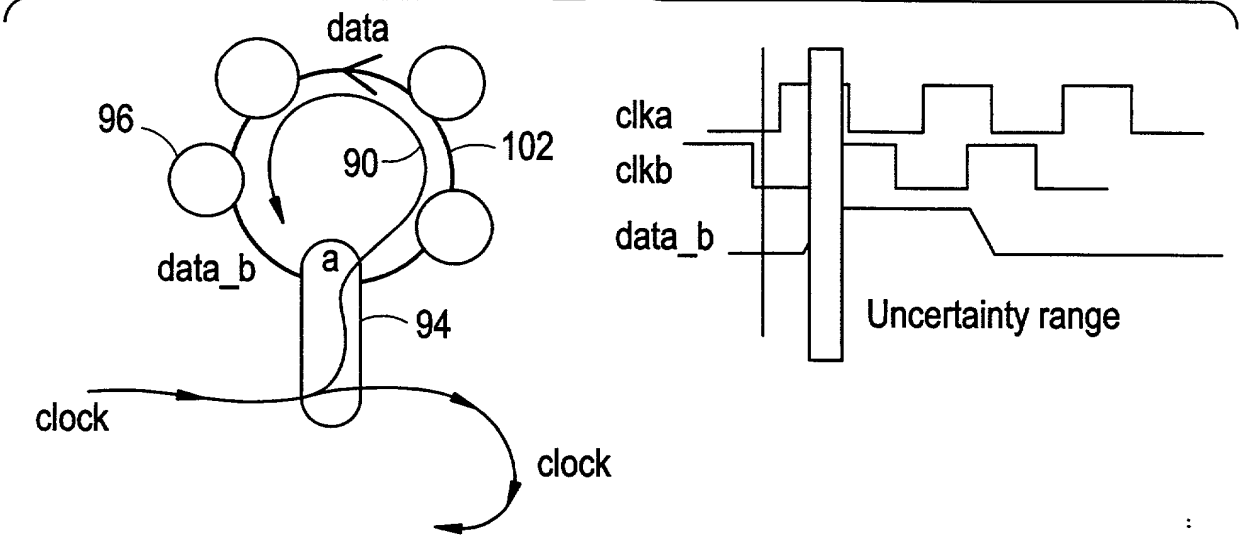


FIG. 11

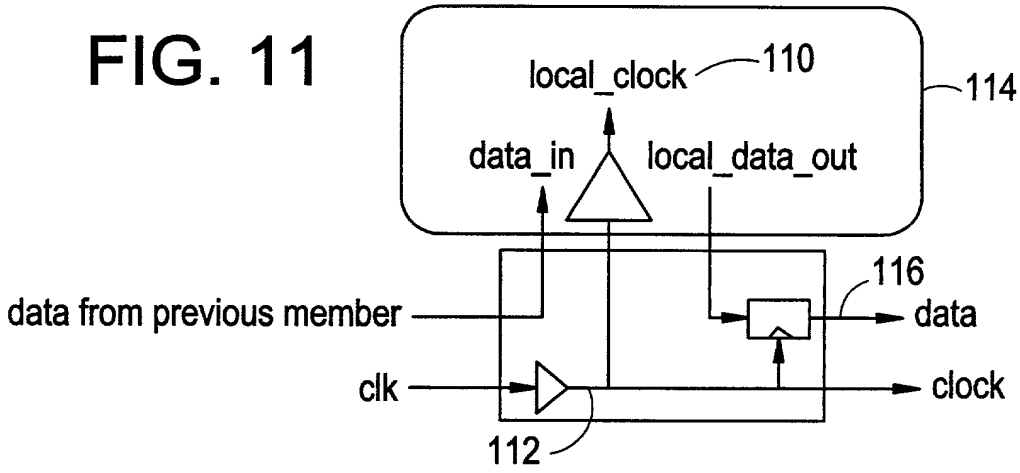


FIG. 12

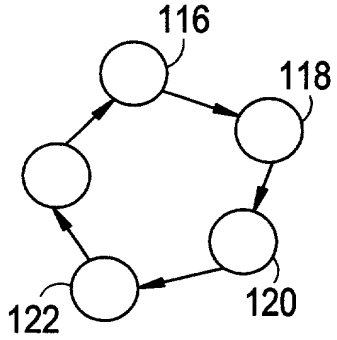
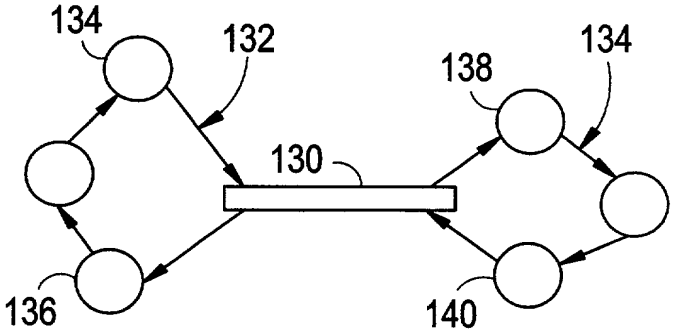


FIG. 13



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FIG. 14

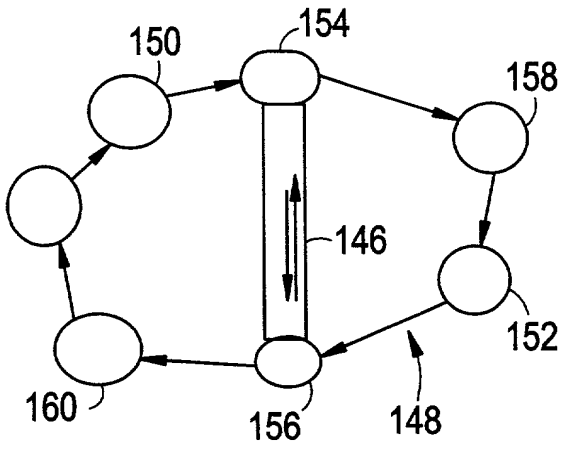


FIG. 15

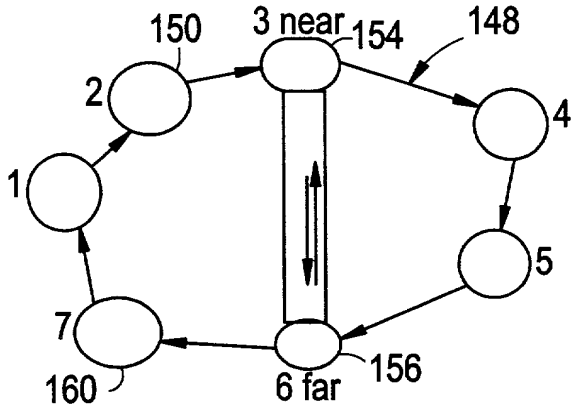
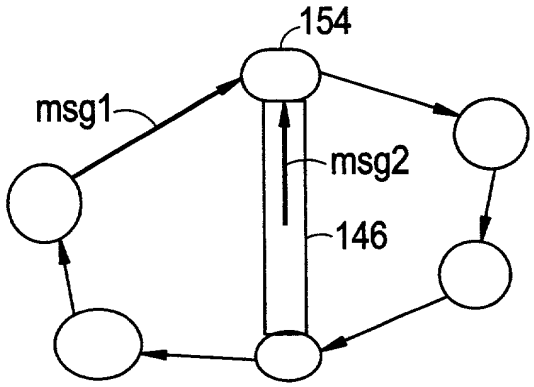


FIG. 16



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FIG. 17

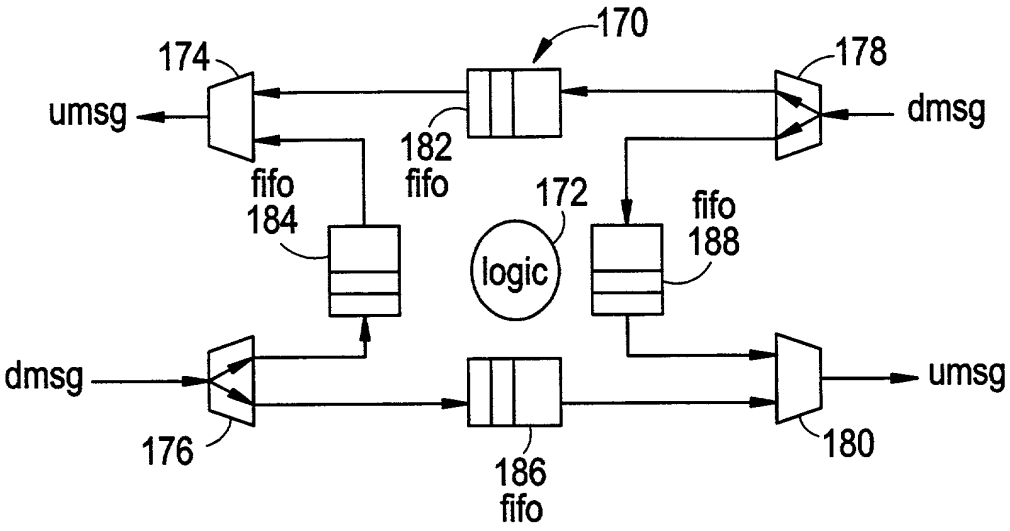


FIG. 18

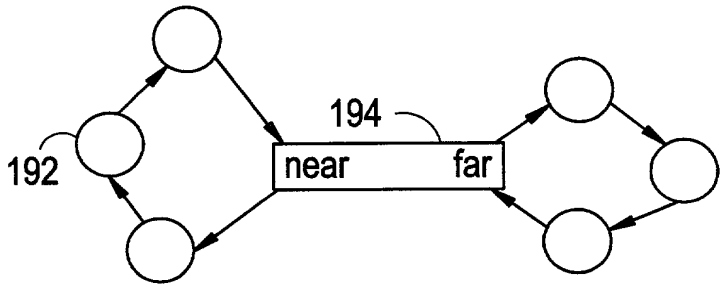
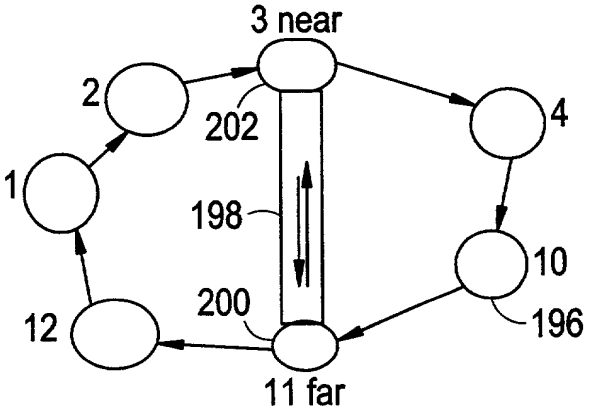


FIG. 19



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FIG. 20

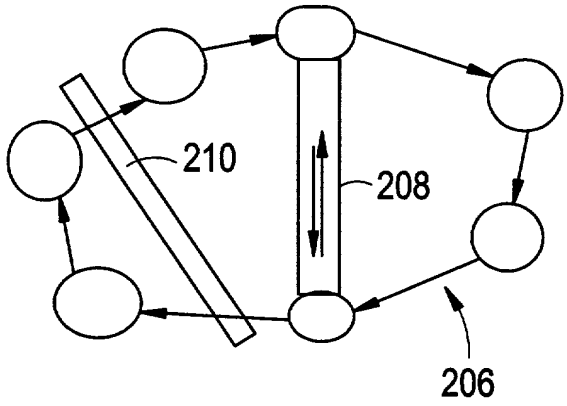


FIG. 21

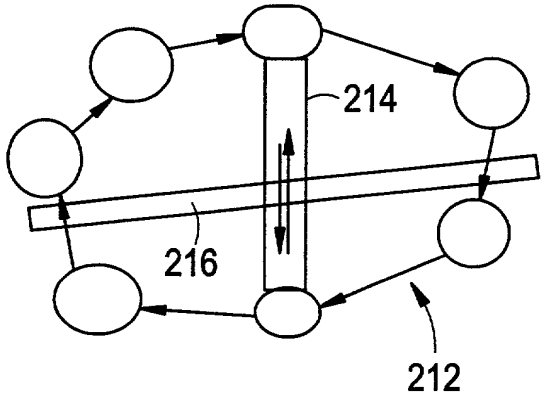
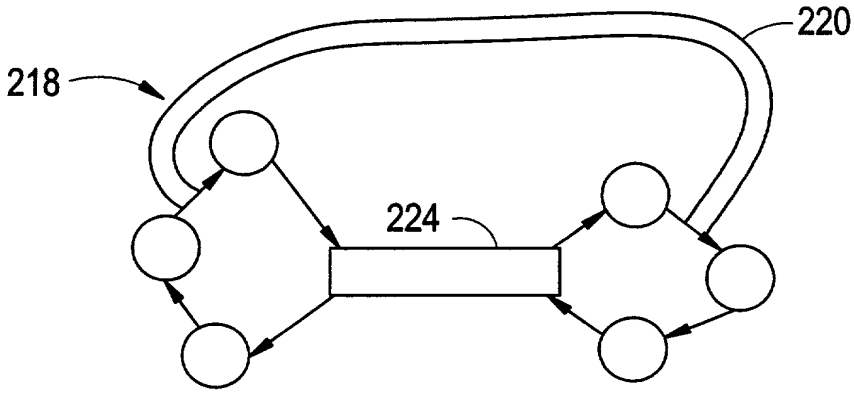


FIG. 22



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FIG. 23

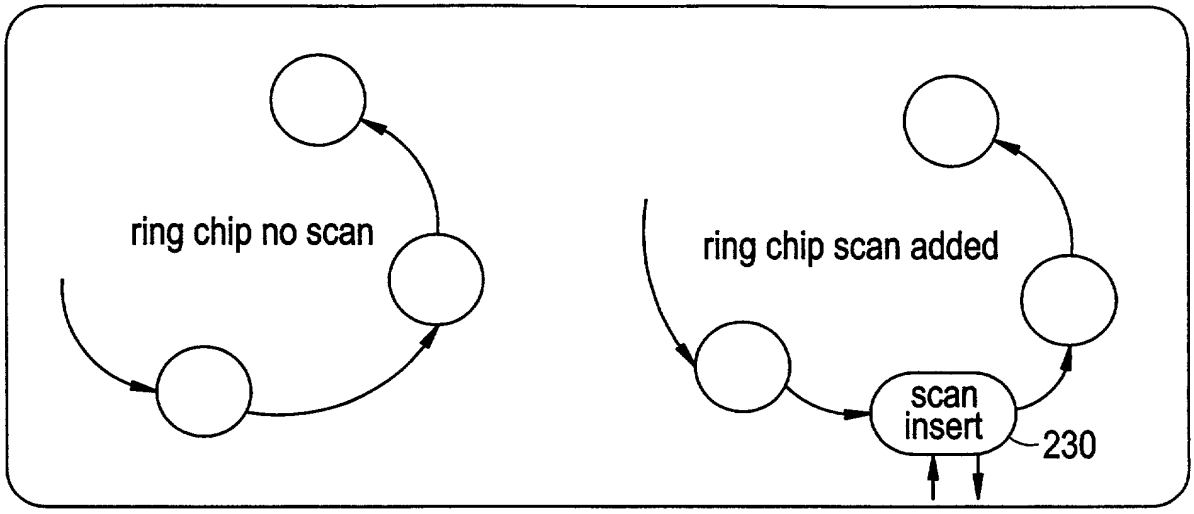


FIG. 24

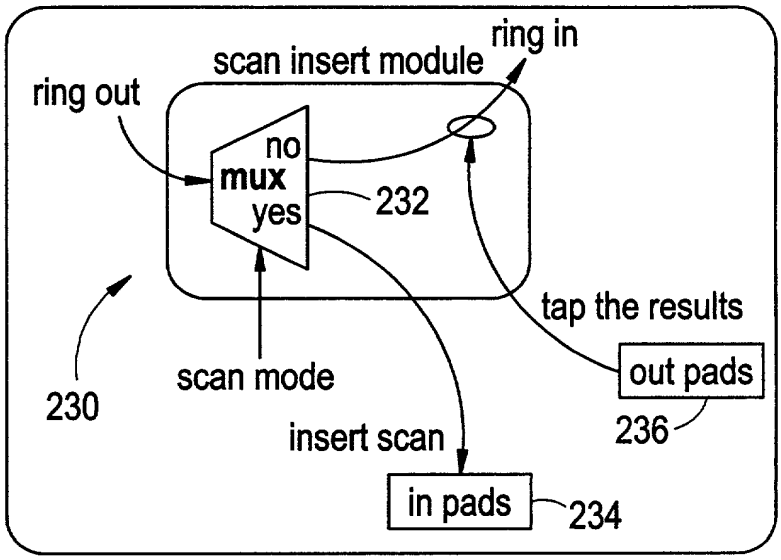
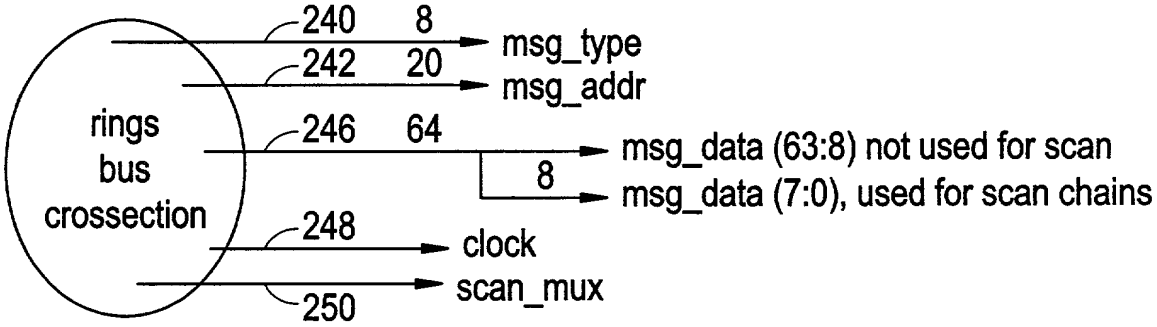


FIG. 25



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FIG. 26

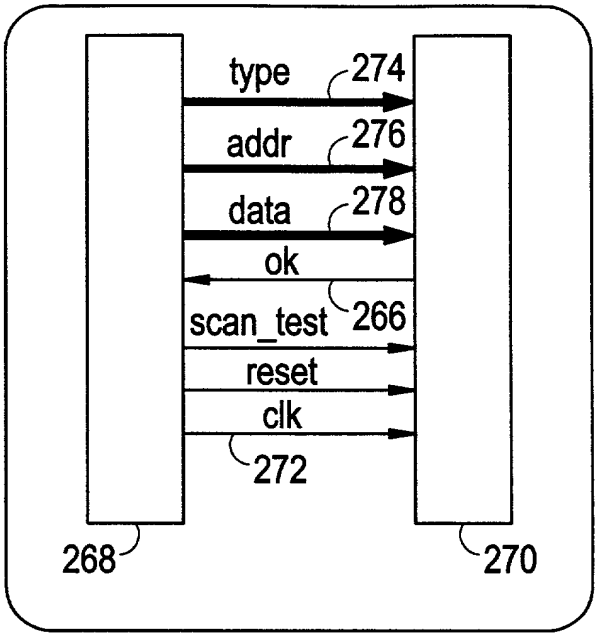
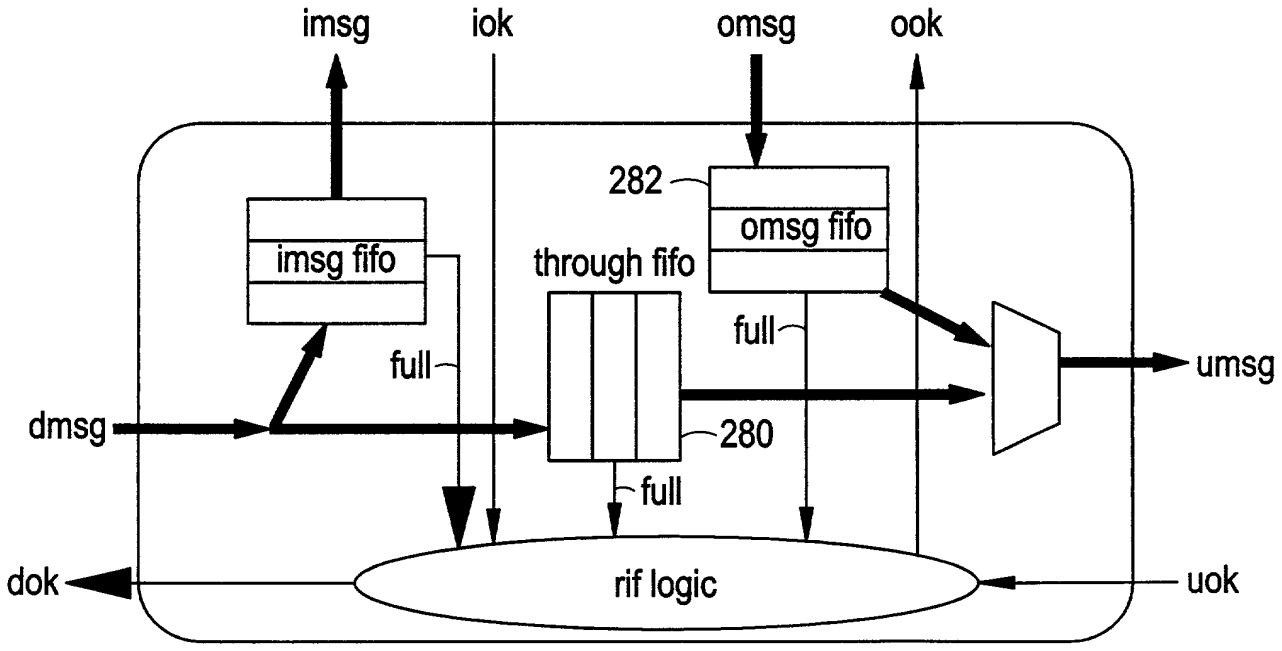


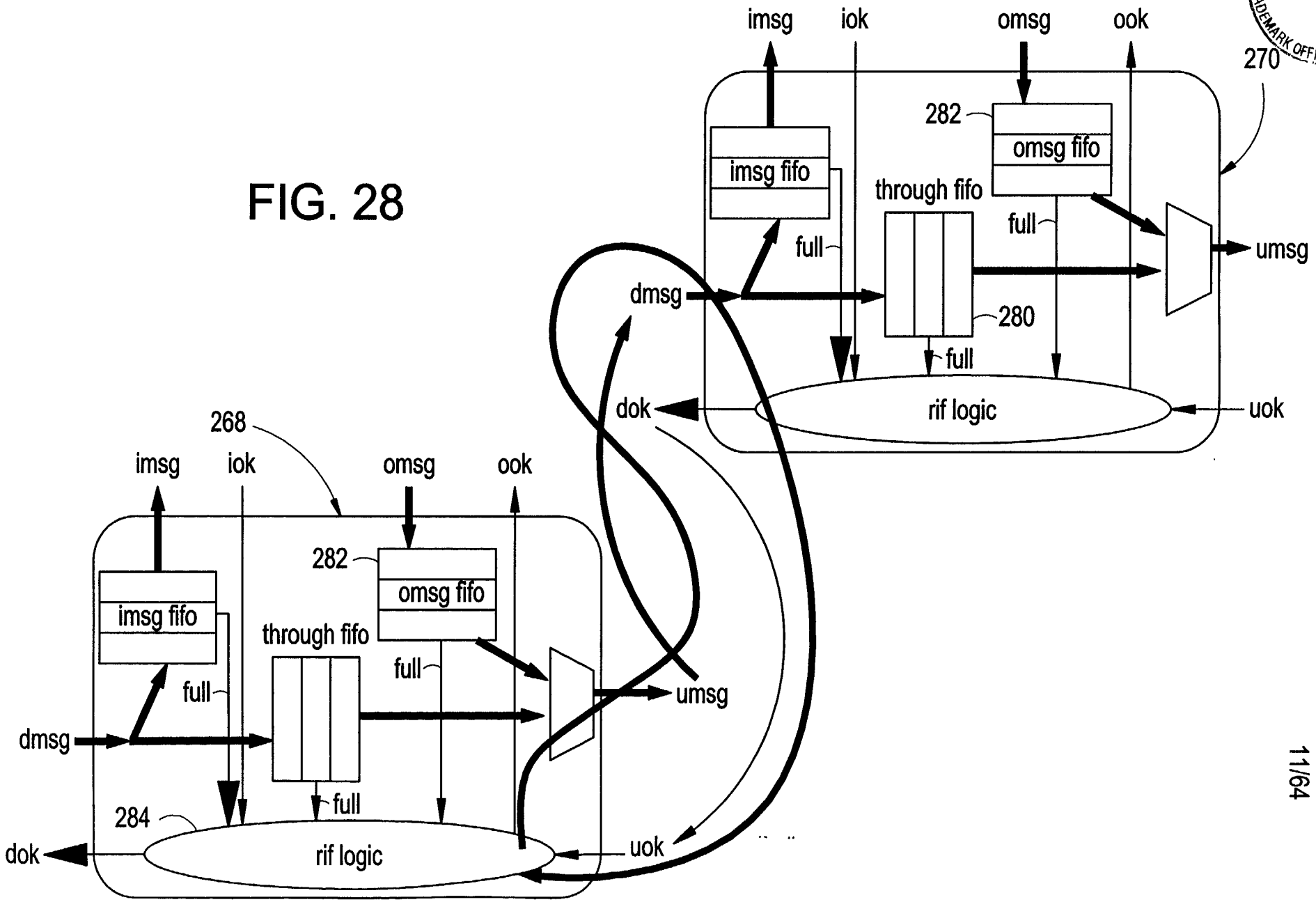
FIG. 27



20h260" 9EEh900T

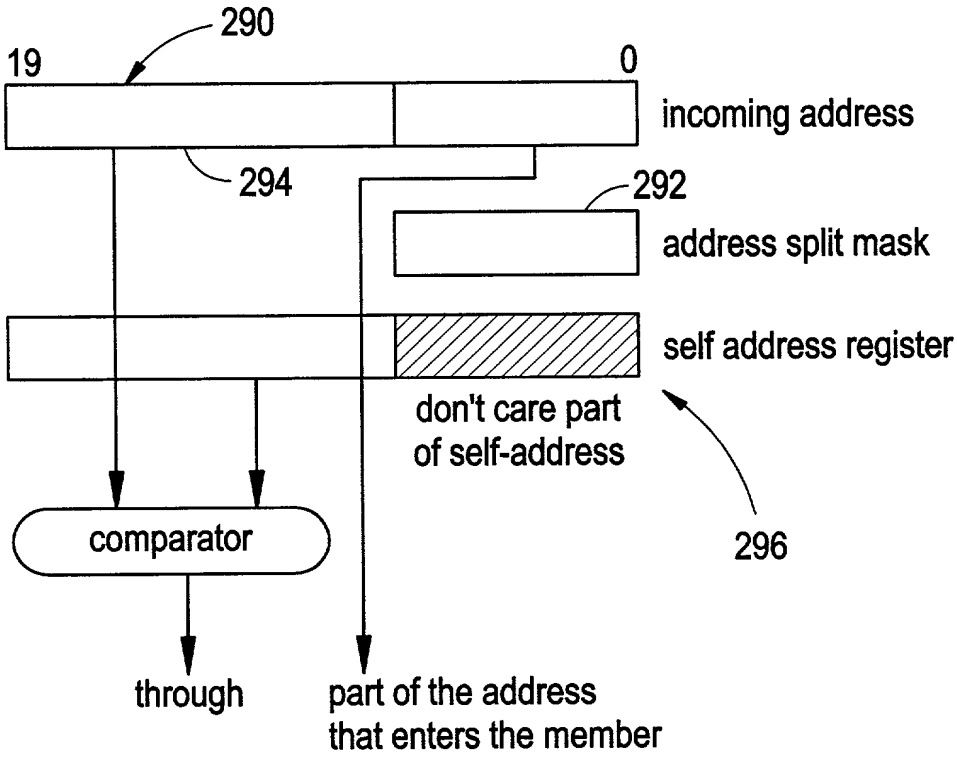
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FIG. 28



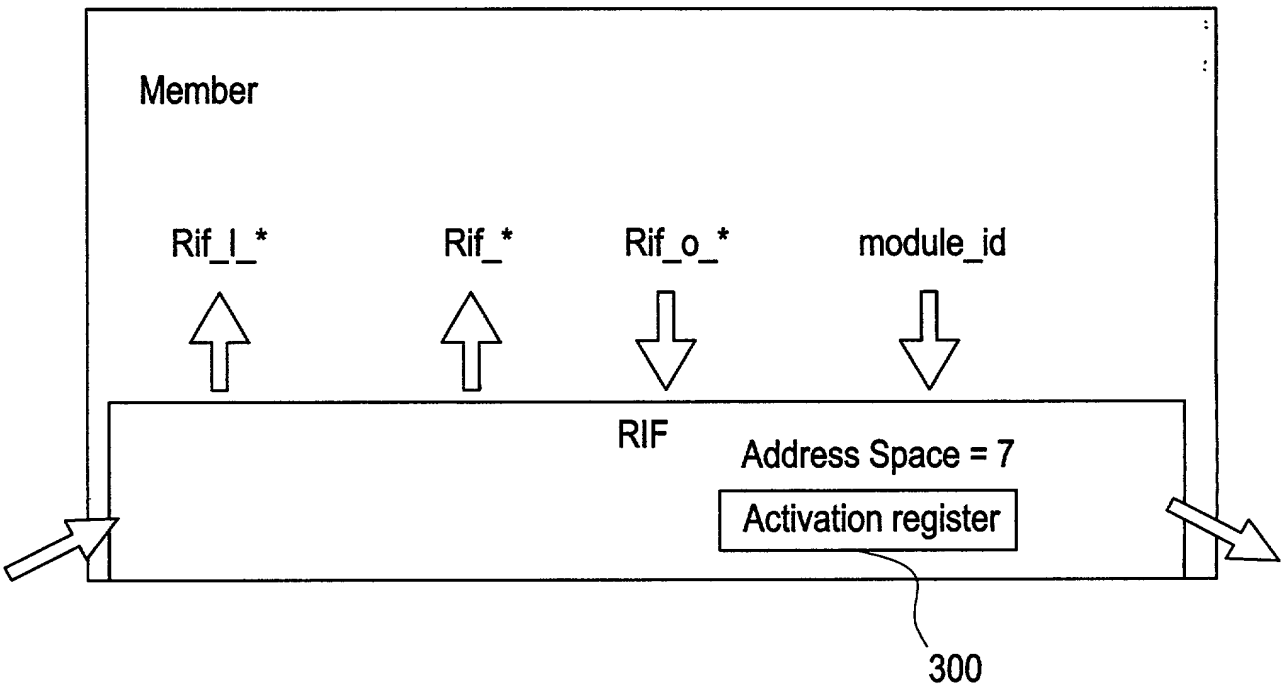
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FIG. 29



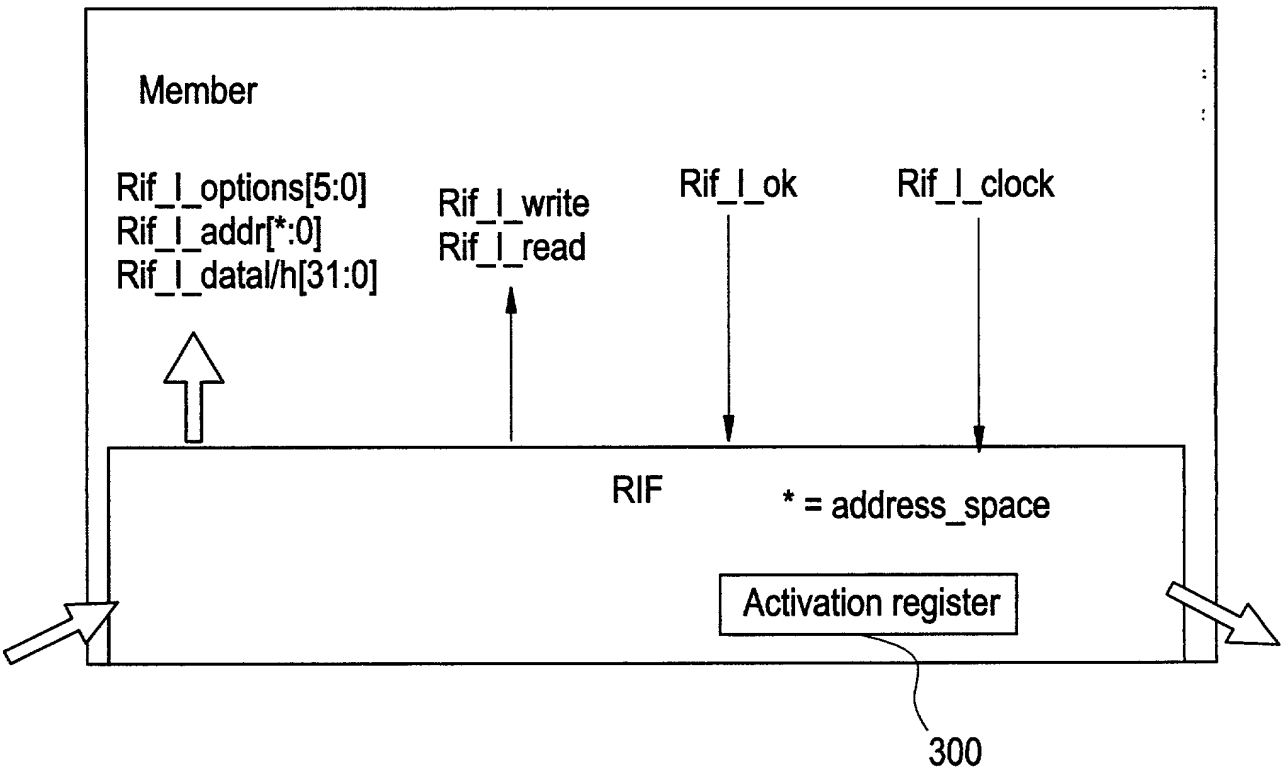
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FIG. 30



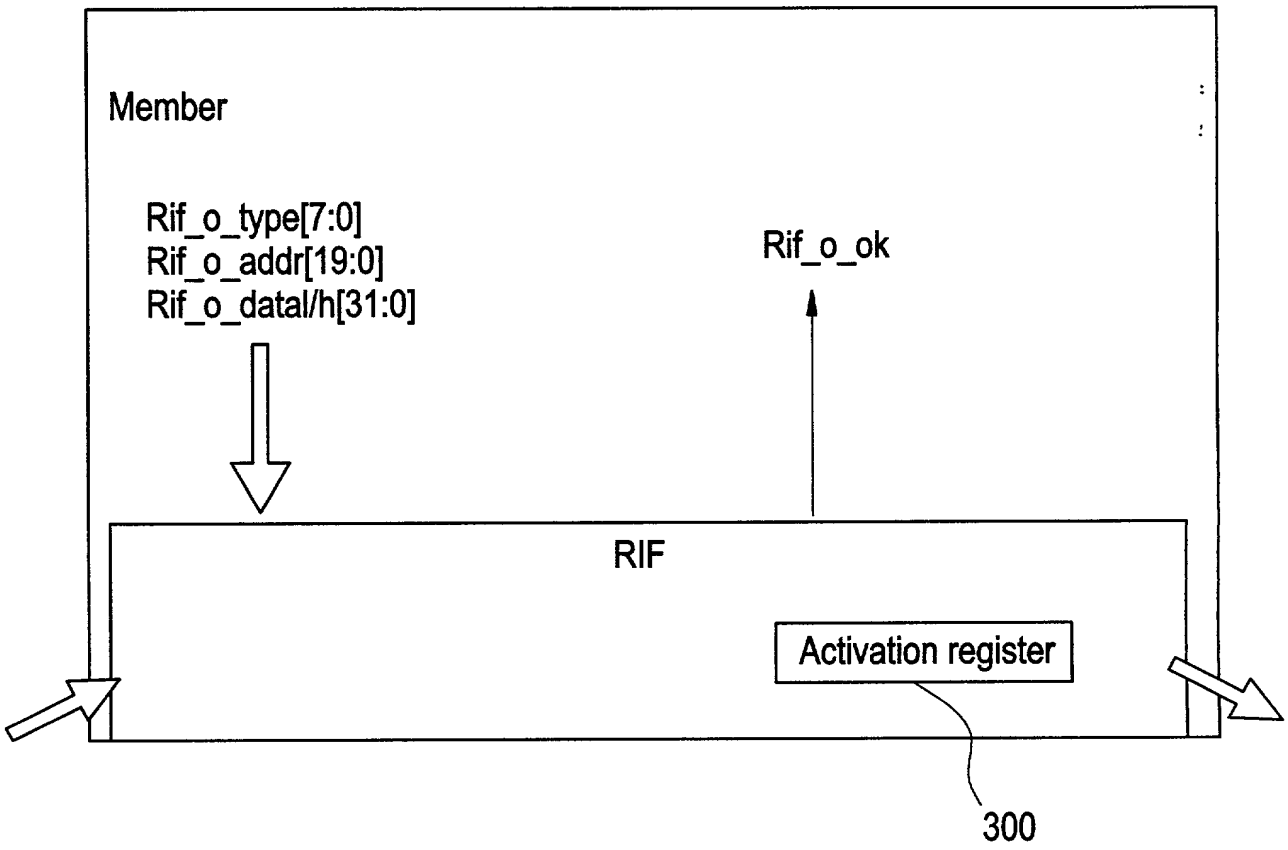
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FIG. 31



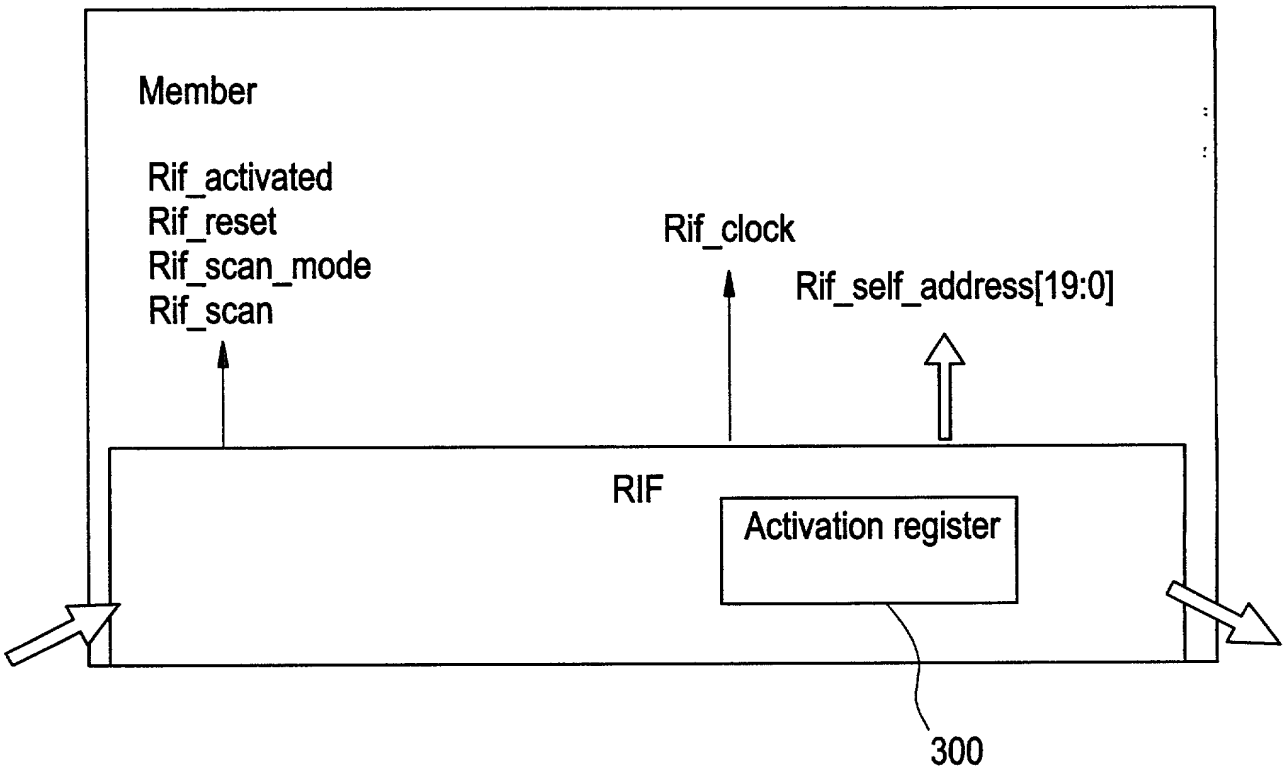
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FIG. 32



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FIG. 33



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FIG. 34

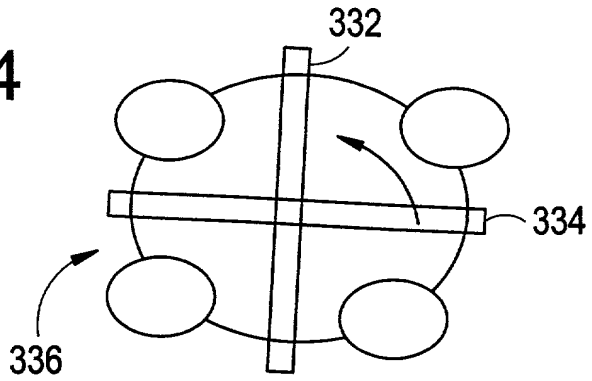


FIG. 35

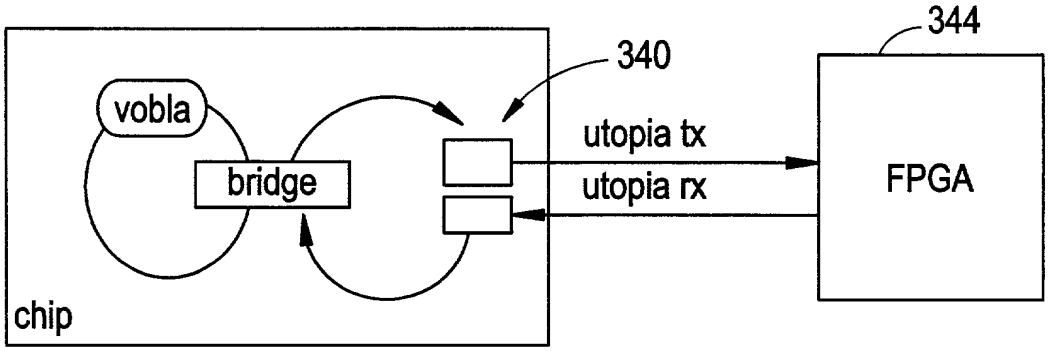
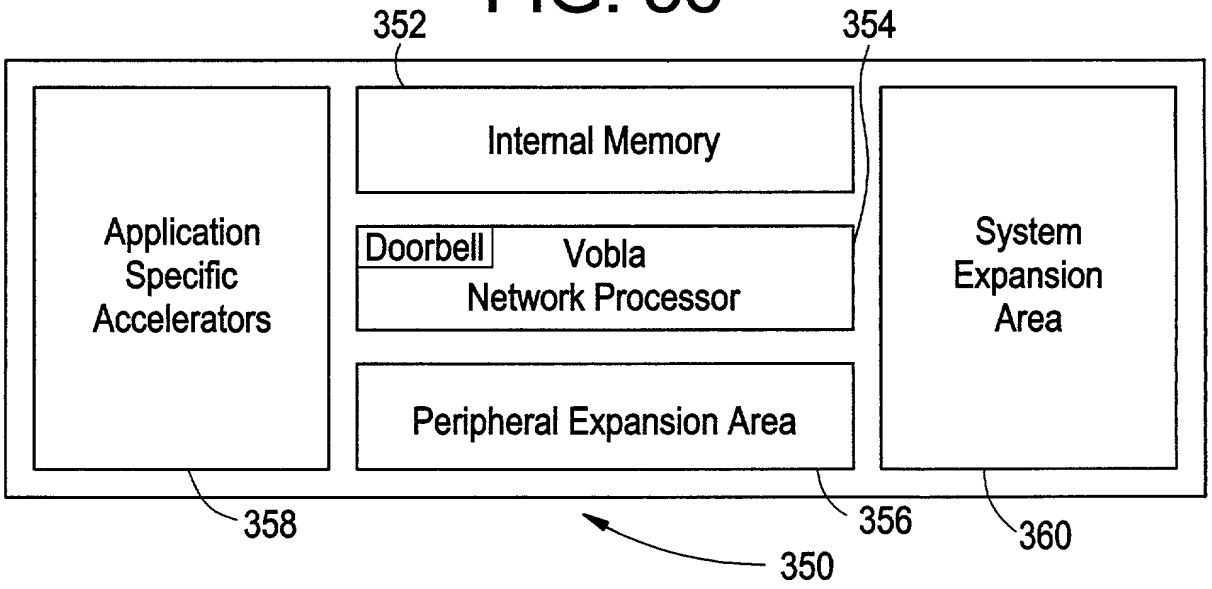


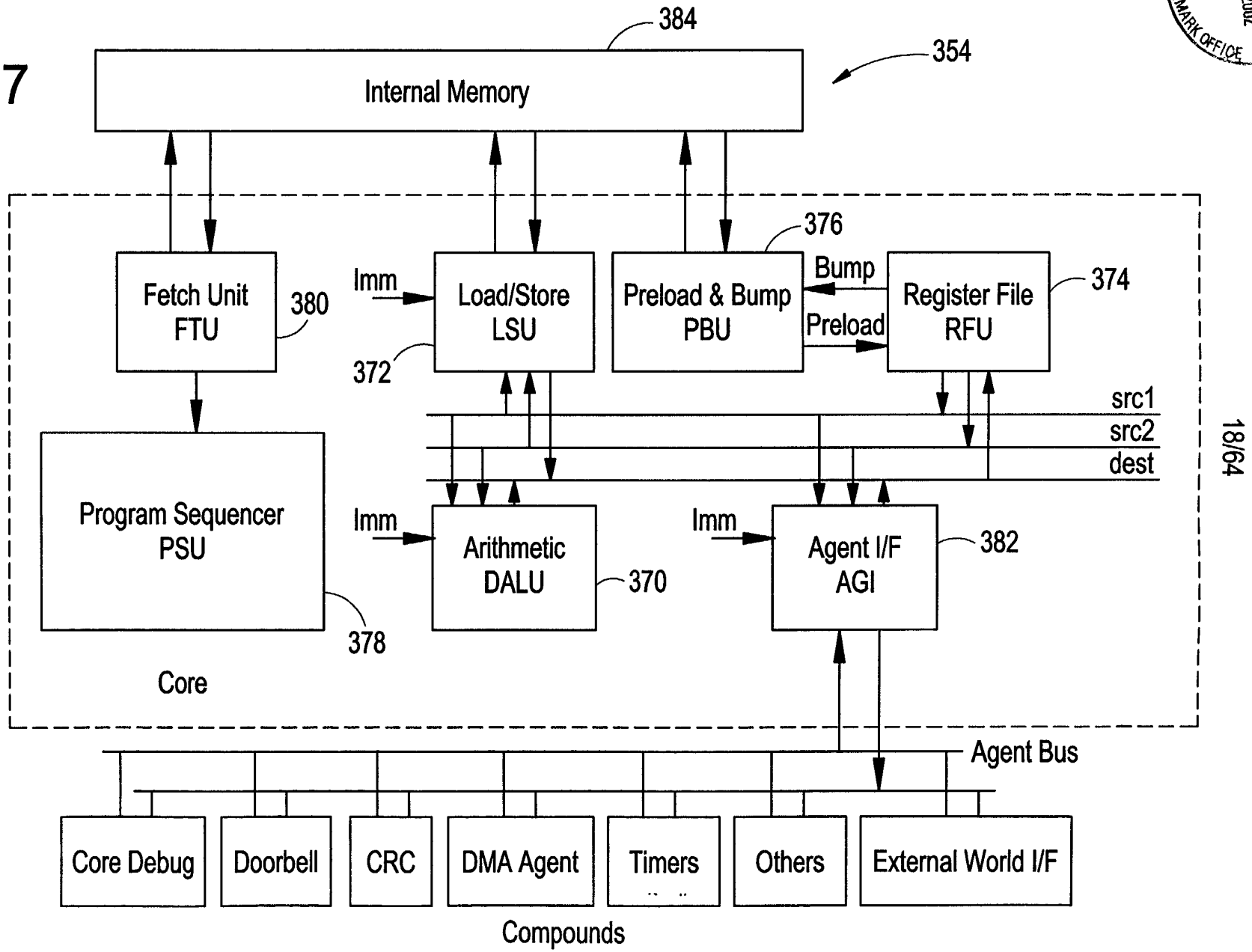
FIG. 36



20h260 "9Ech900T

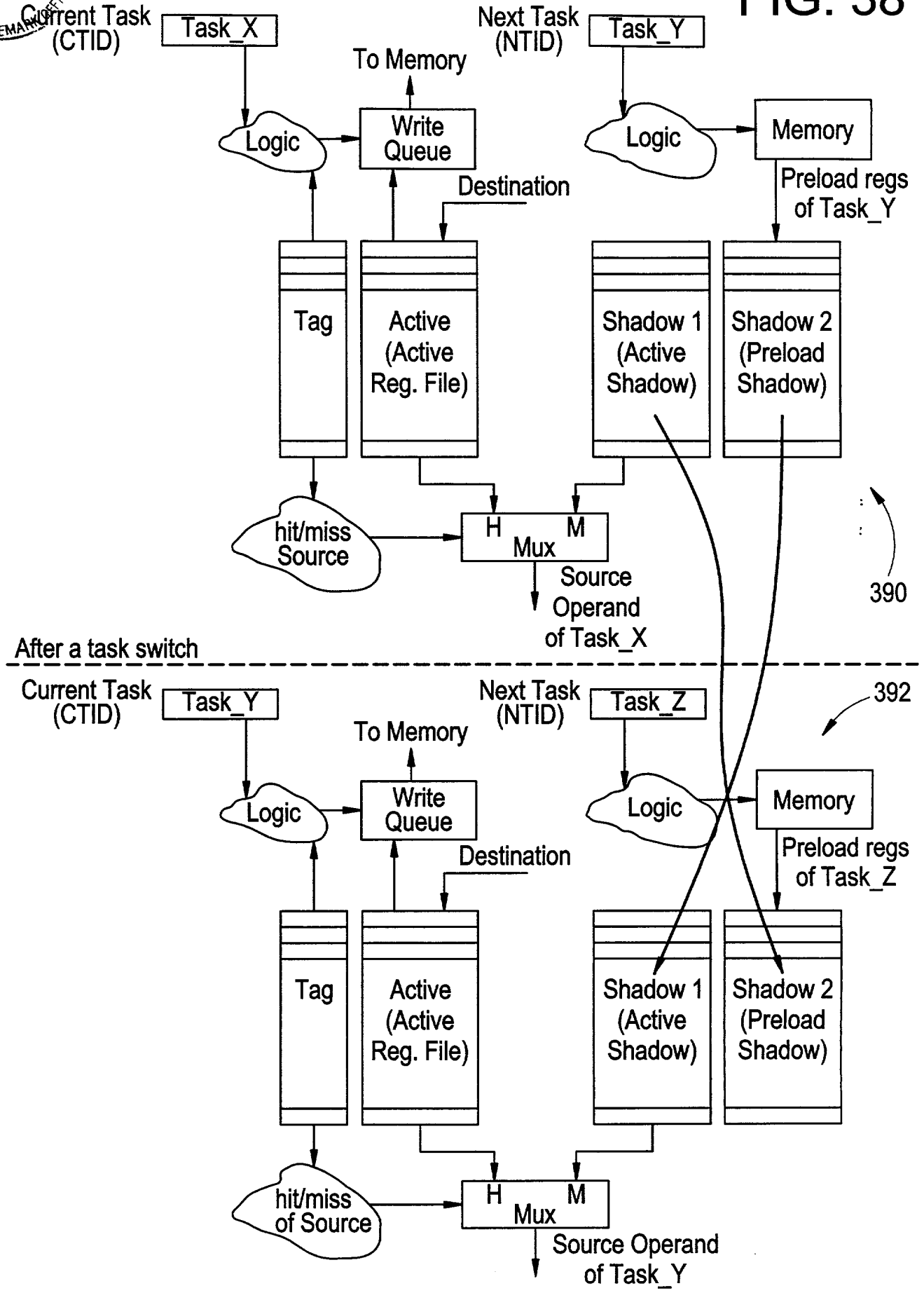
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FIG. 37



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FIG. 38



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FIG. 39

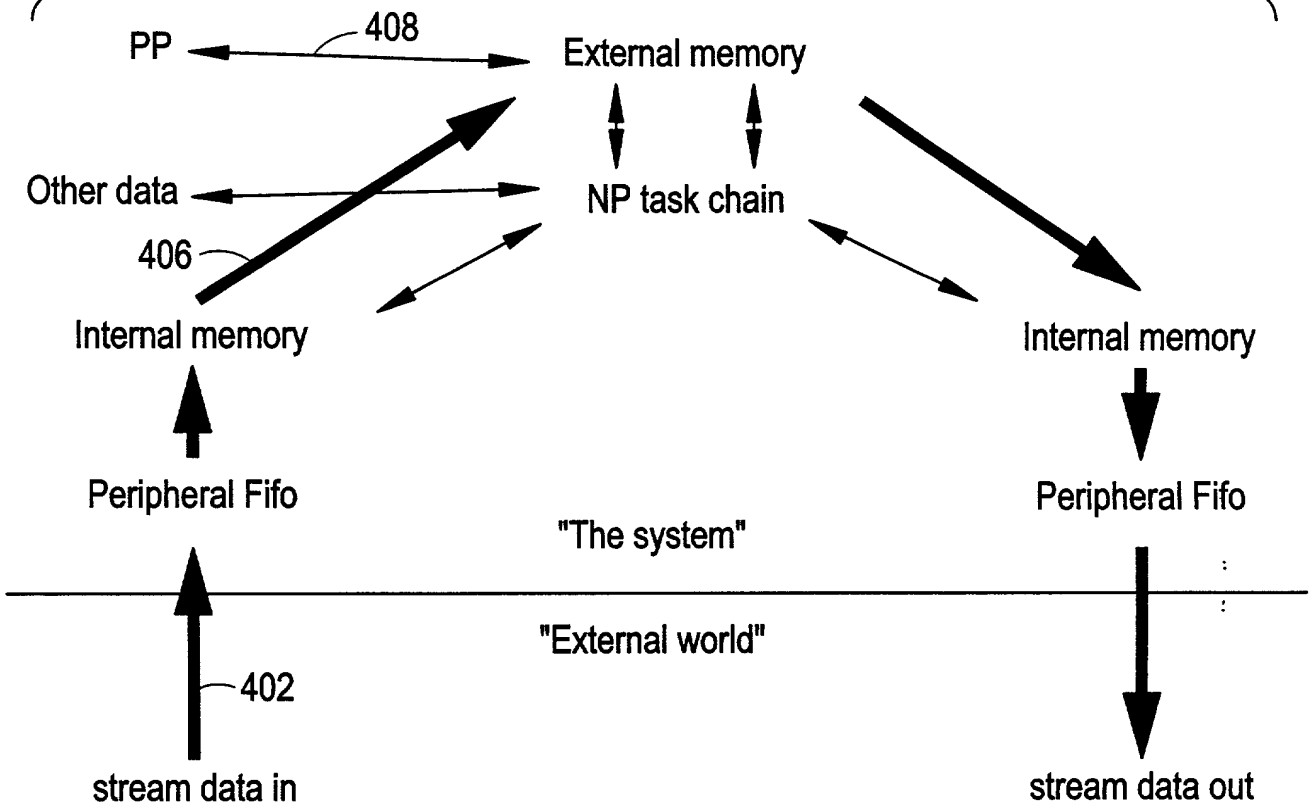
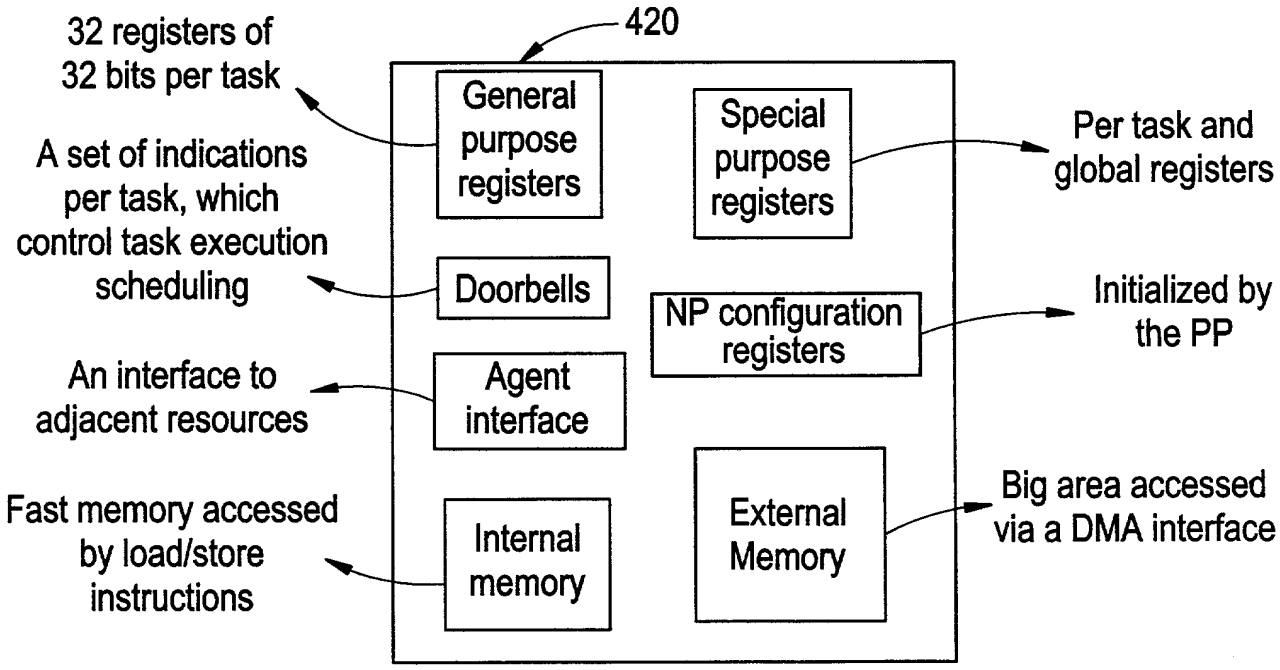


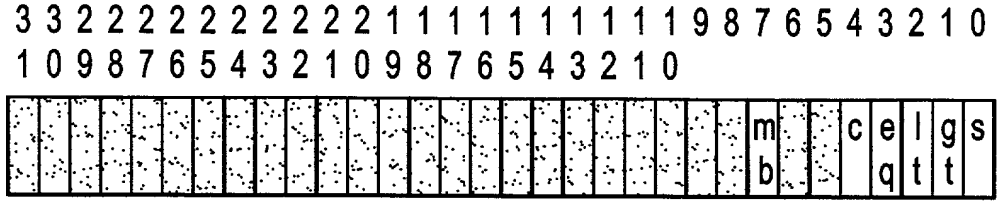
FIG. 40



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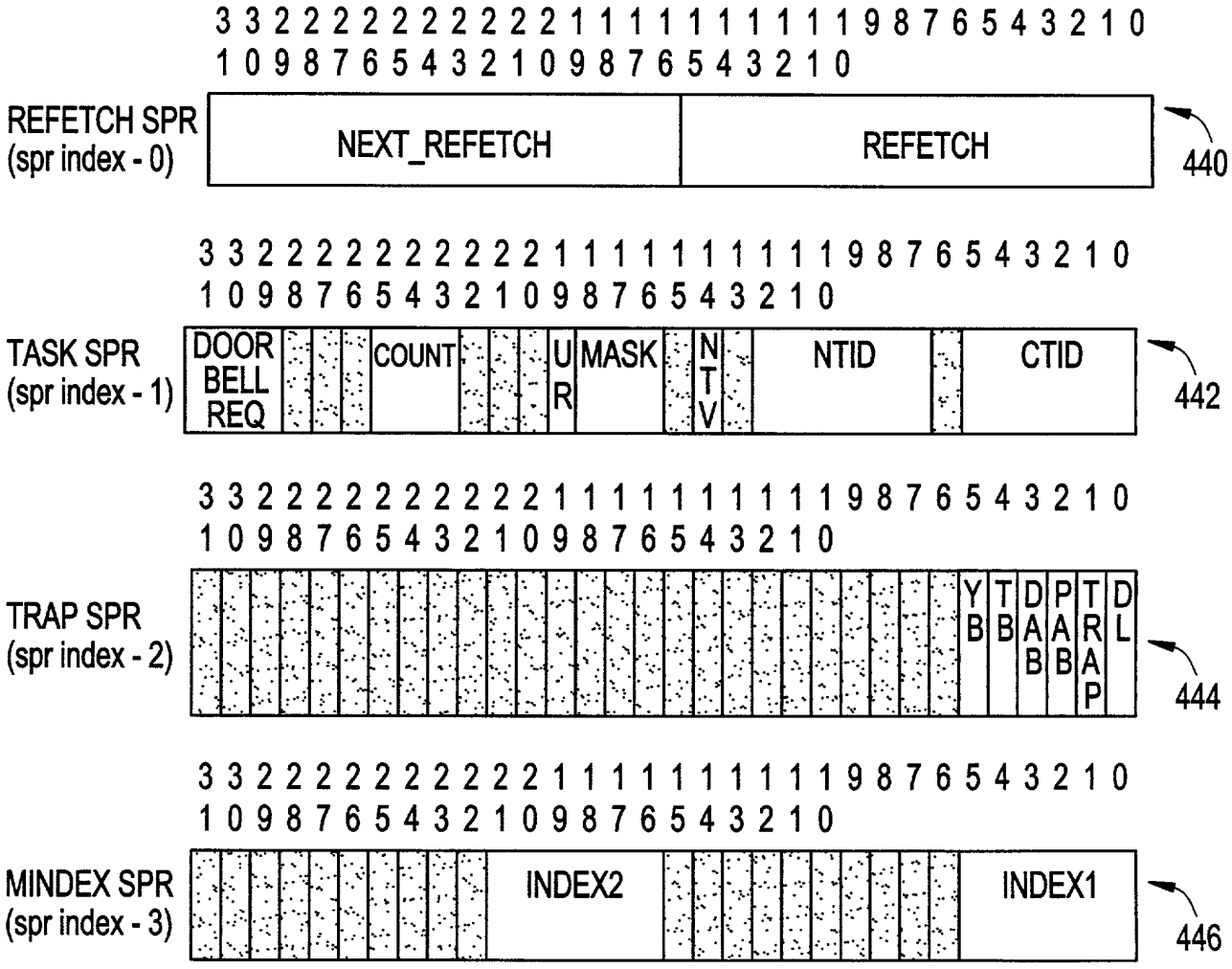
FIG. 41

R1 register



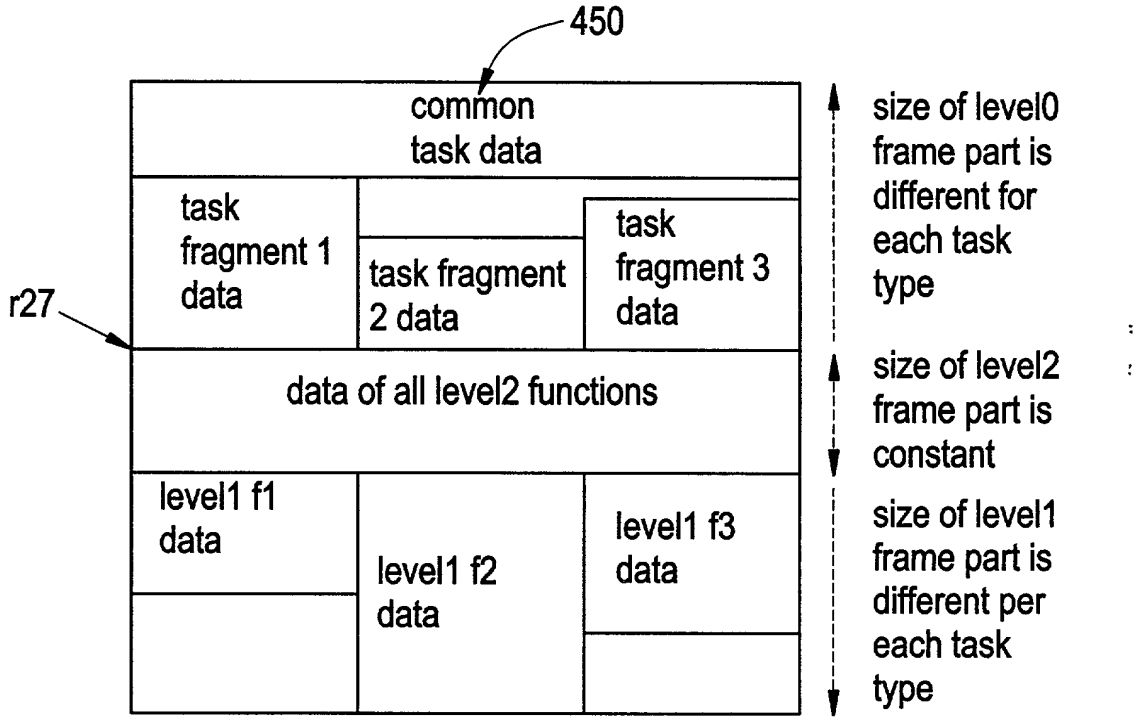
s - sticky bit
eq - equal/zero
lt - less then/negative
gt - greater then/positive
c - carry
mb - reflection of the RAM mult-reader busy indication

FIG. 42



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FIG. 43

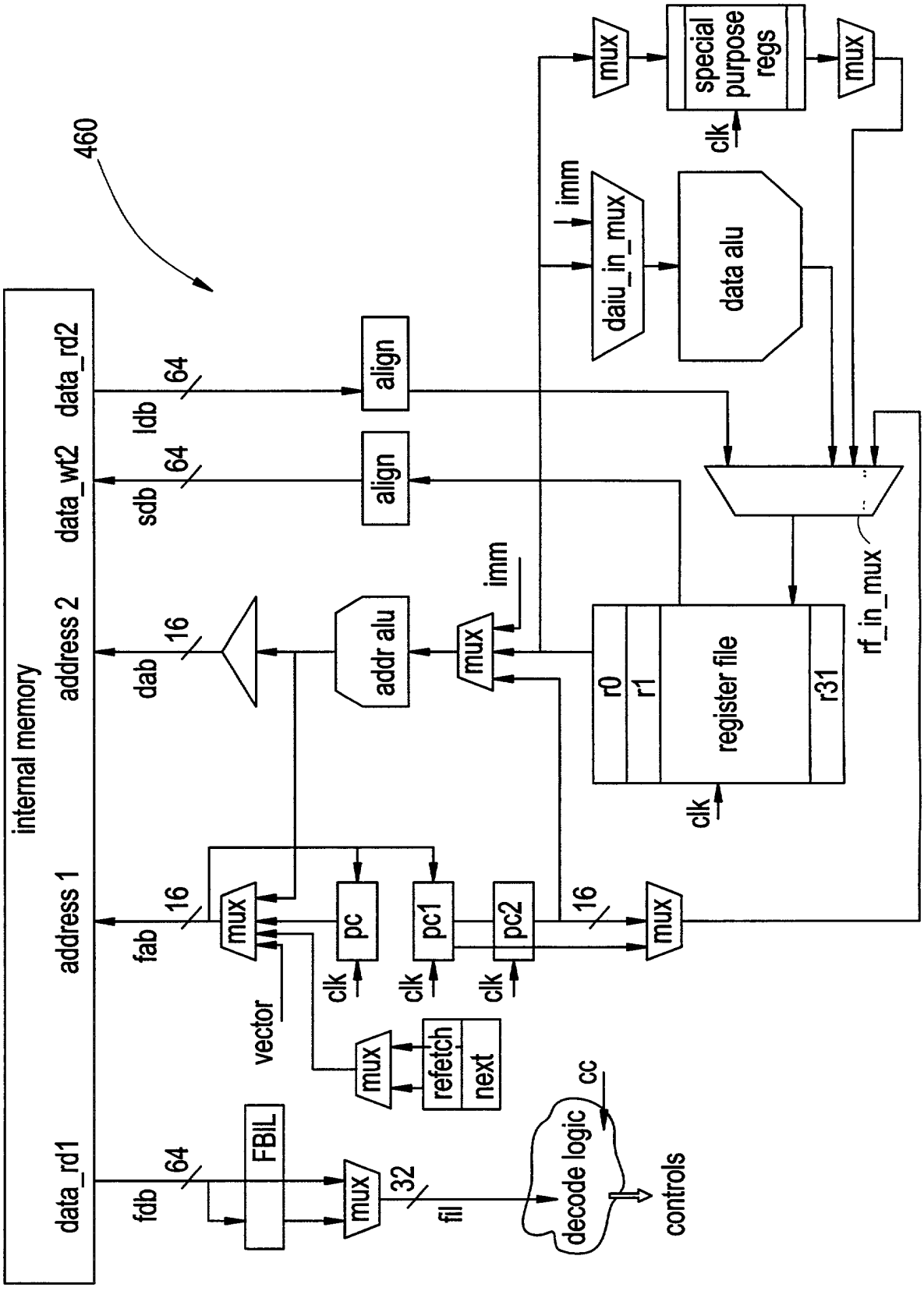


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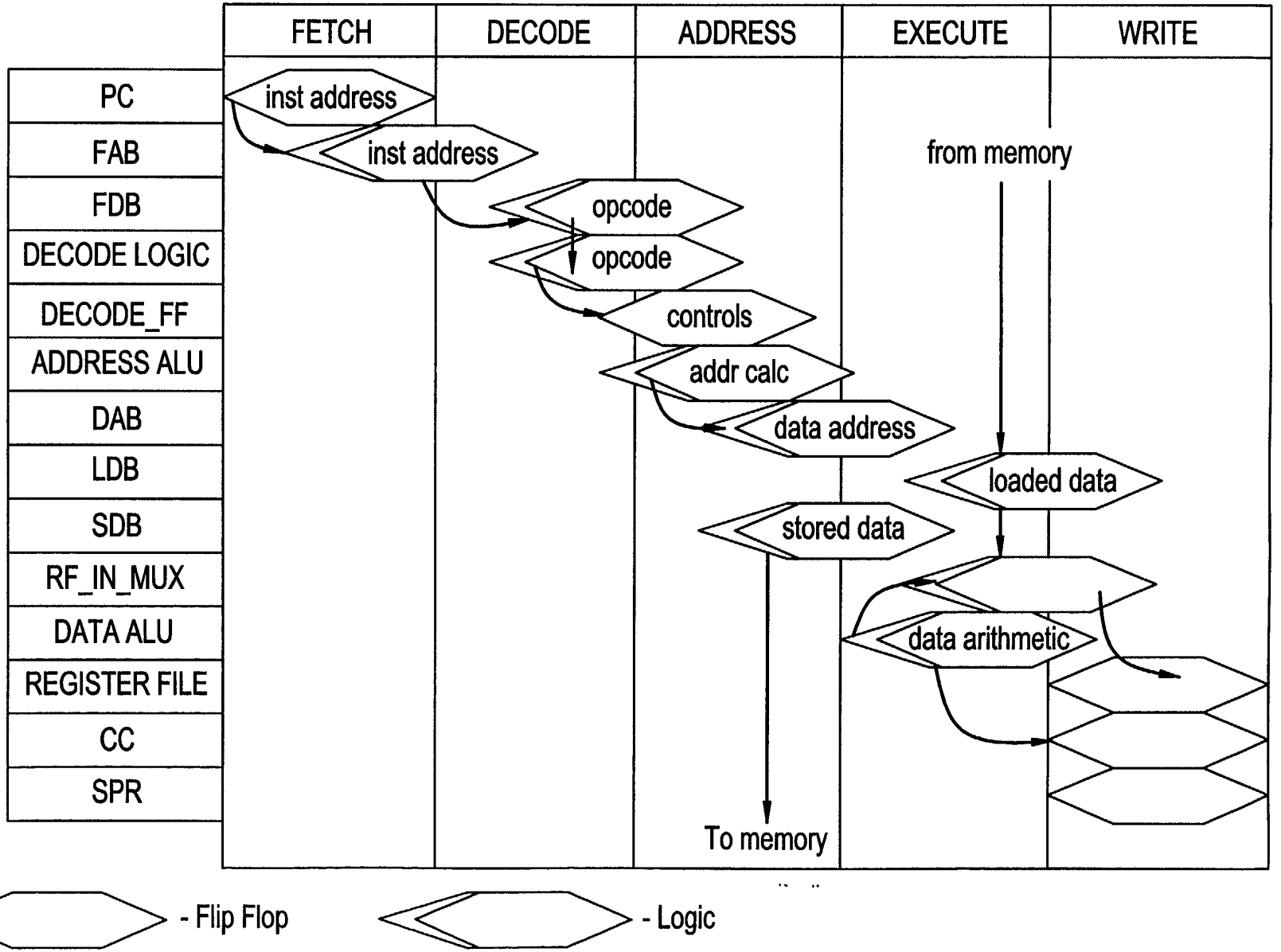
FIG. 44



20h260" 9ECEh900F

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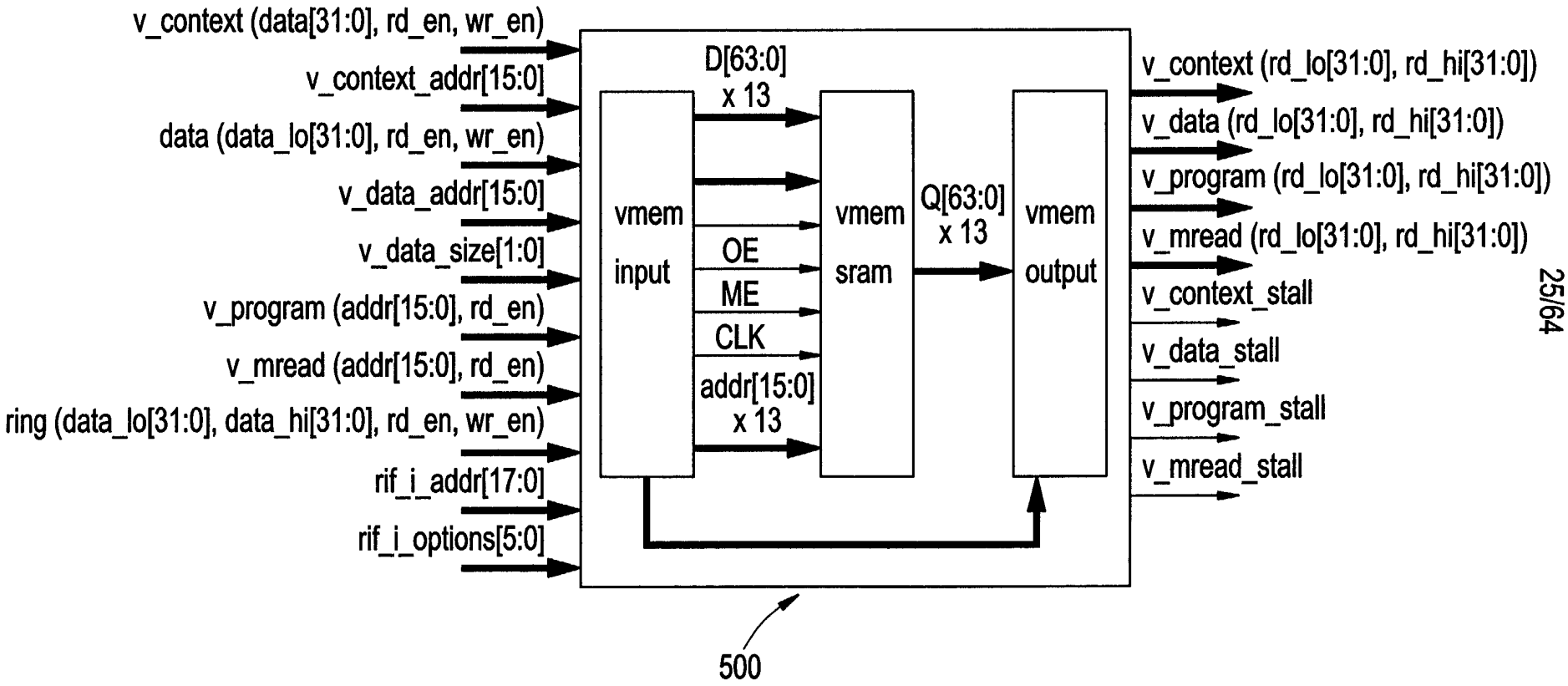
FIG. 45



20k260* 9Sch90Of

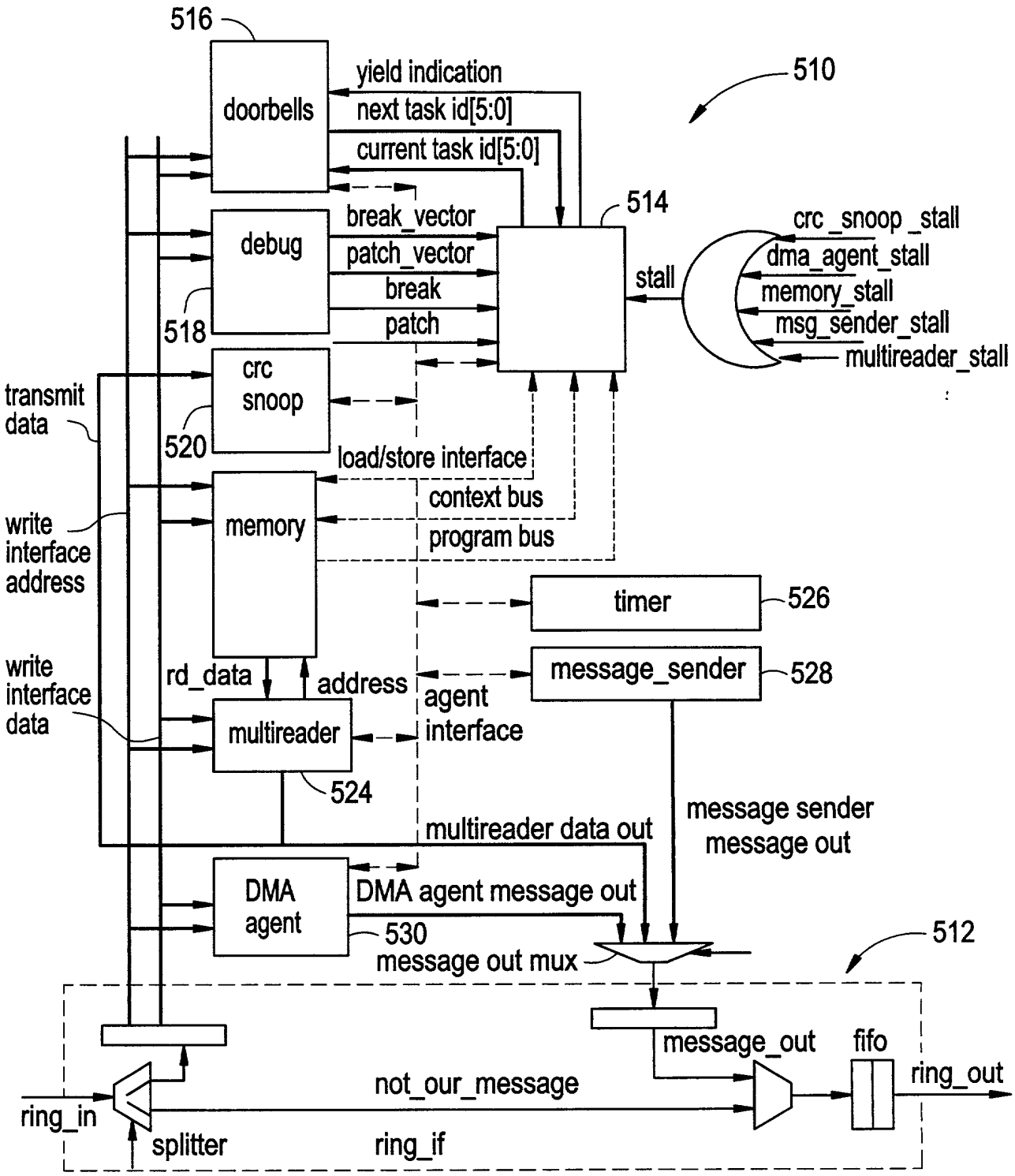
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FIG. 46



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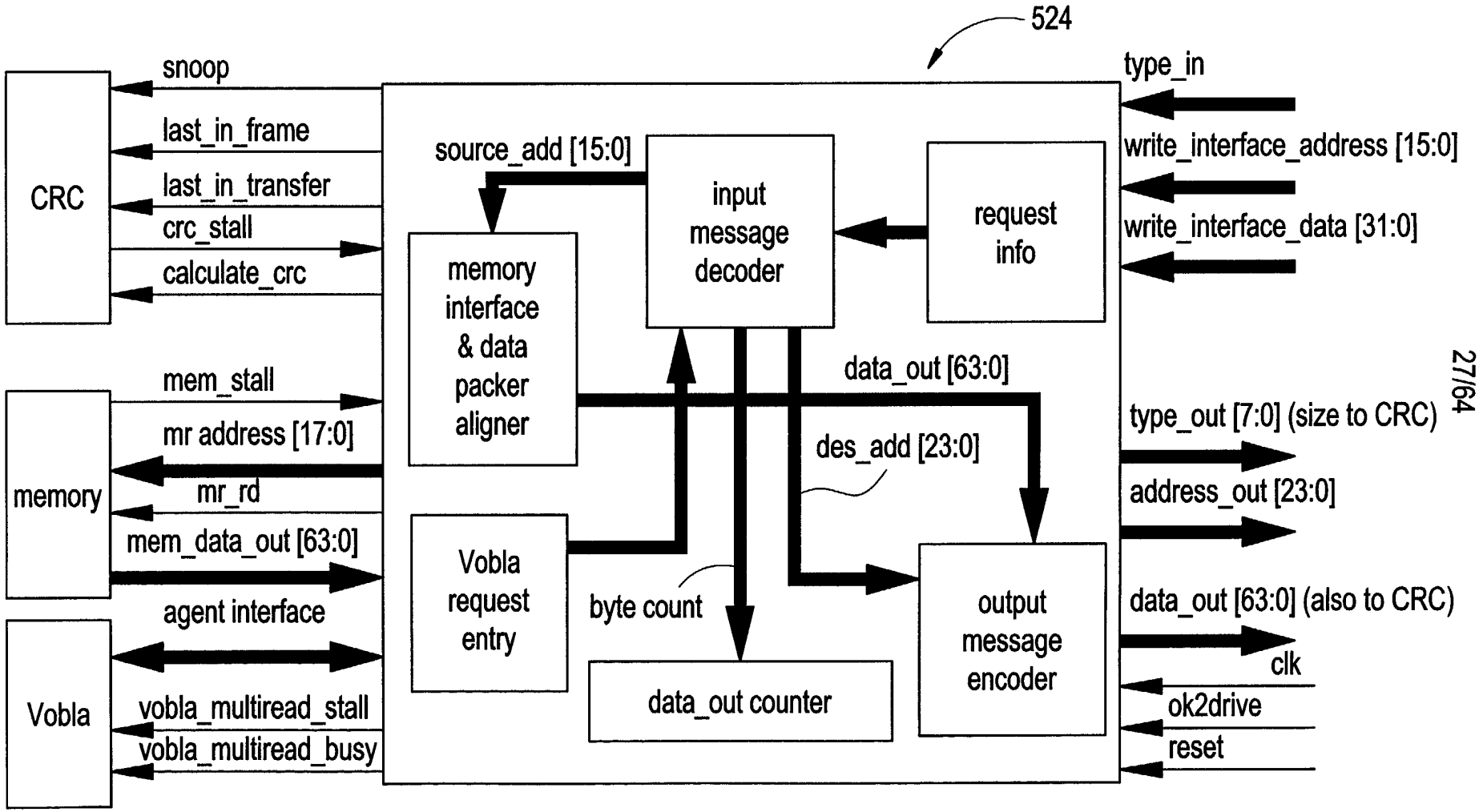
FIG. 47



20h260" 9ECEh900T

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FIG. 48



20m260" 9c5h900t

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FIG. 49

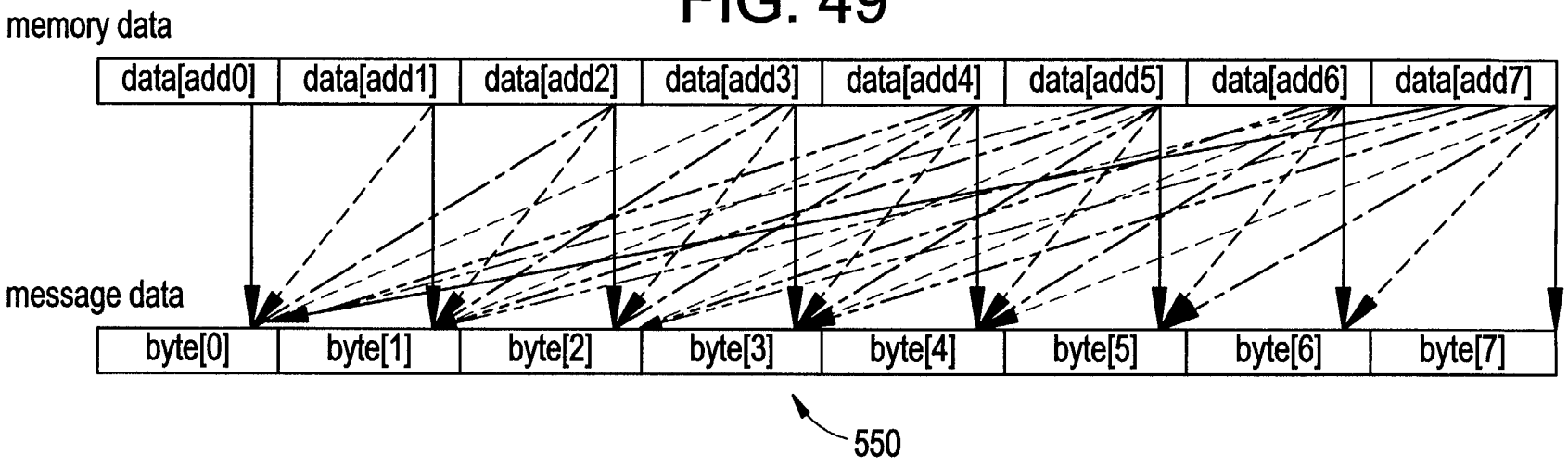
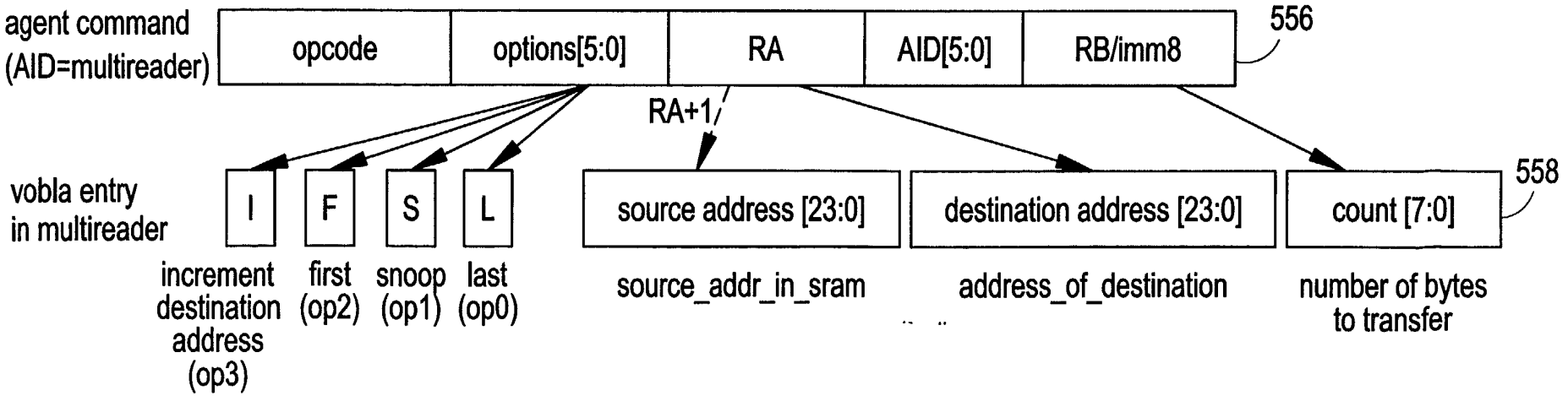
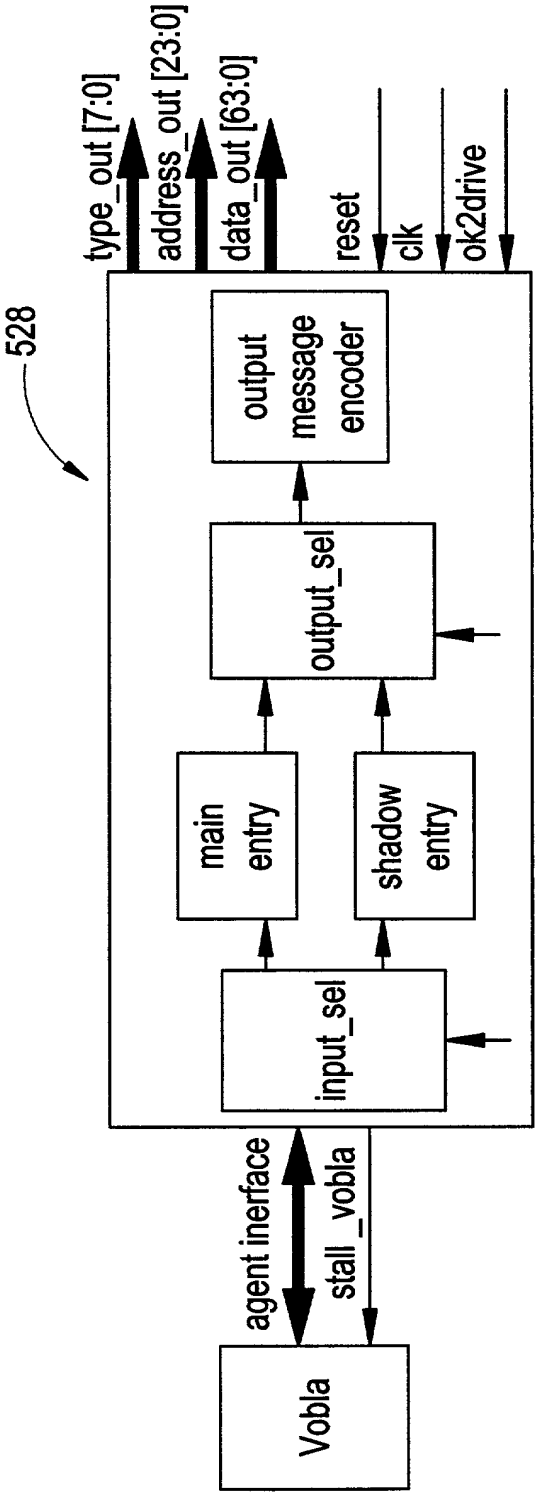


FIG. 50



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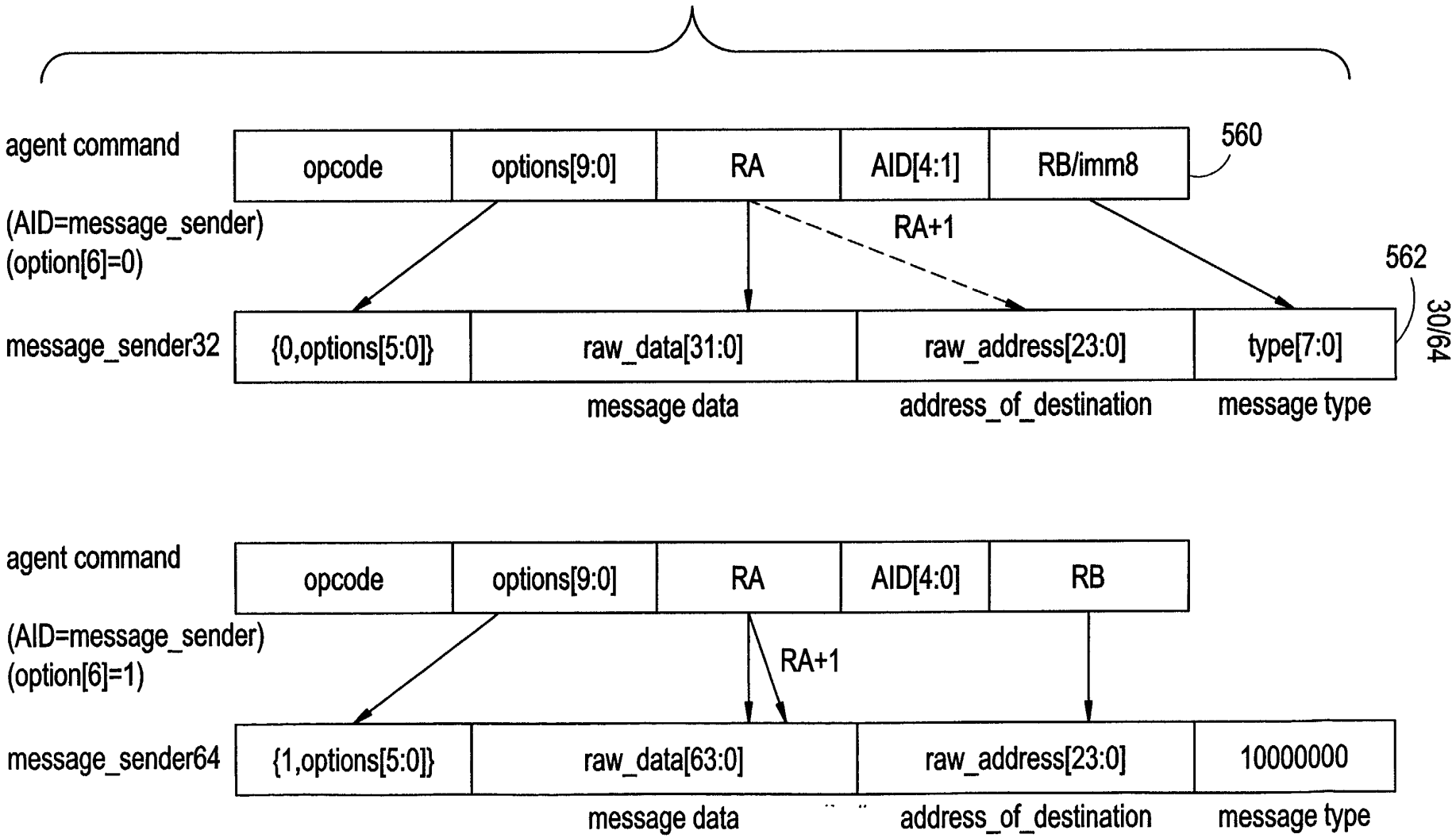
FIG. 51



20k260 9ec5h900T

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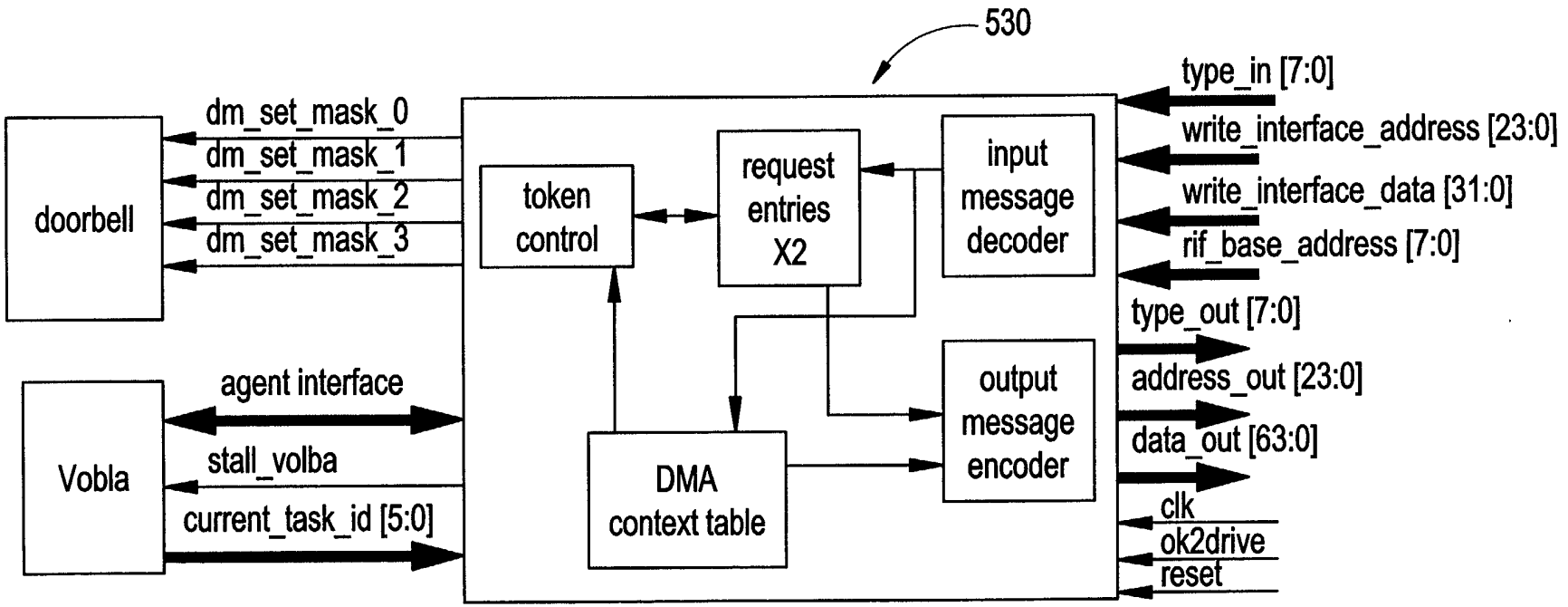
FIG. 52



20h260" 9Ec4h90Dt

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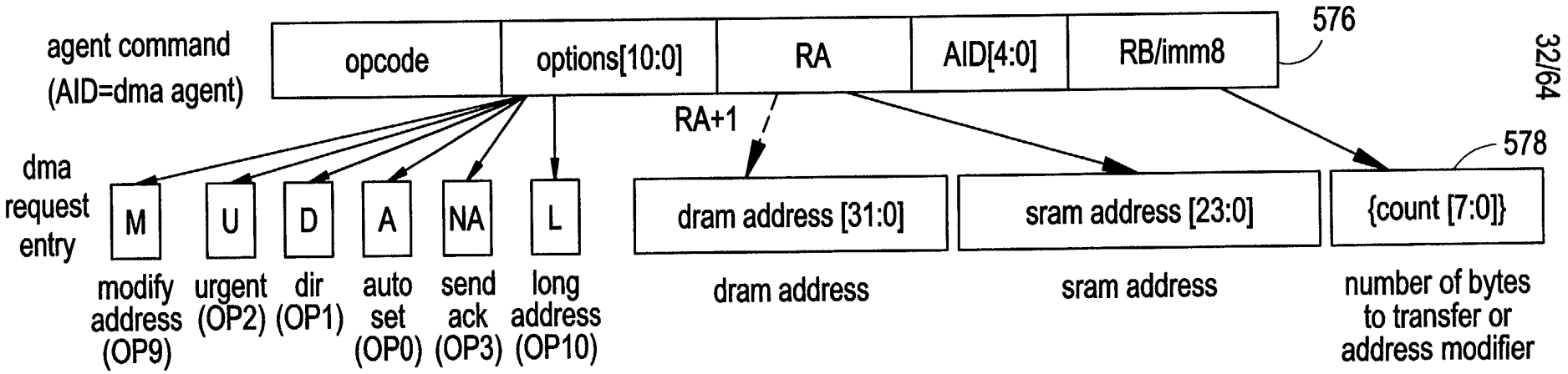
FIG. 53



20t260' 9Et9007

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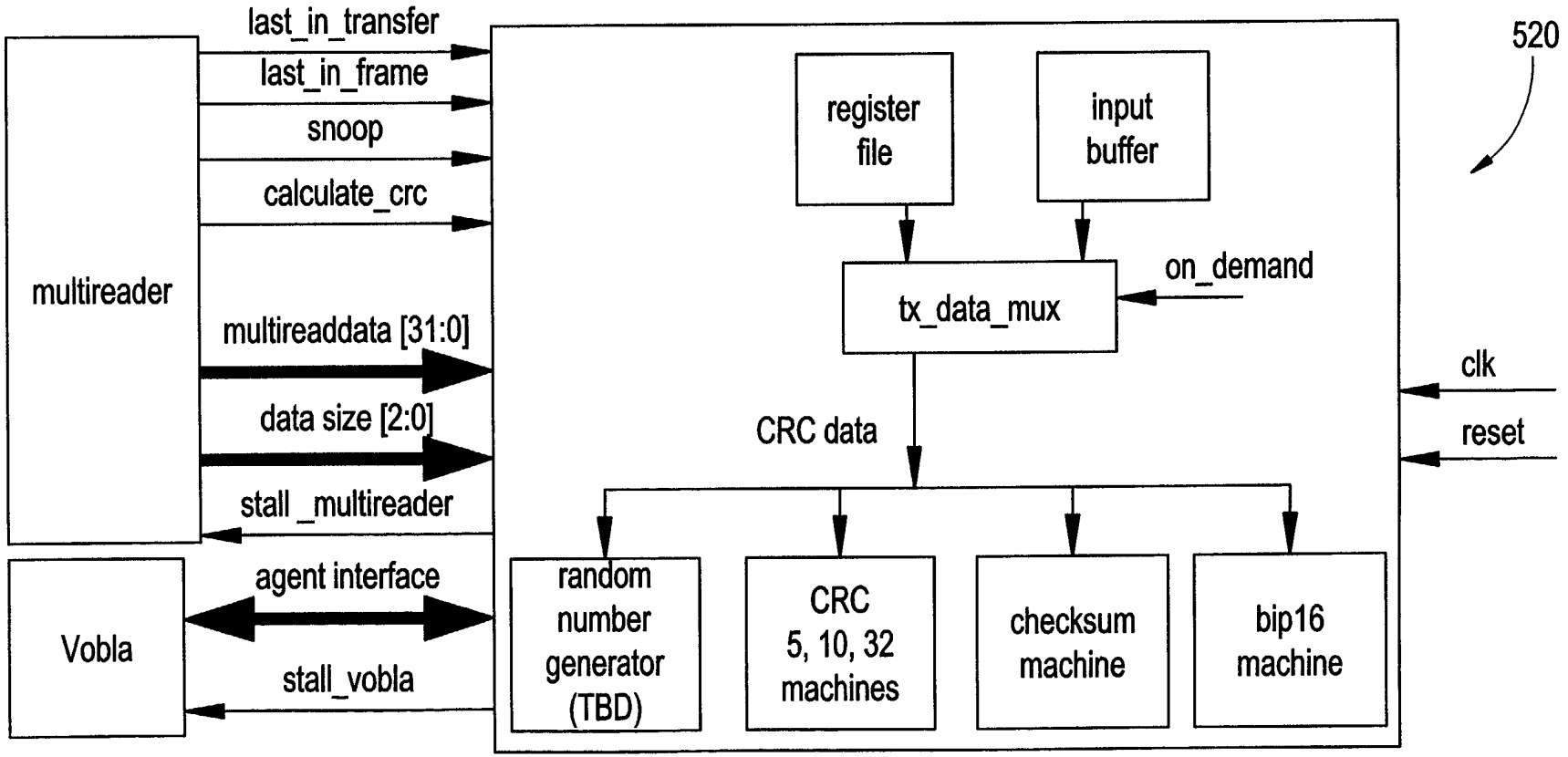
FIG. 54



20h260' 9ec6h90dT

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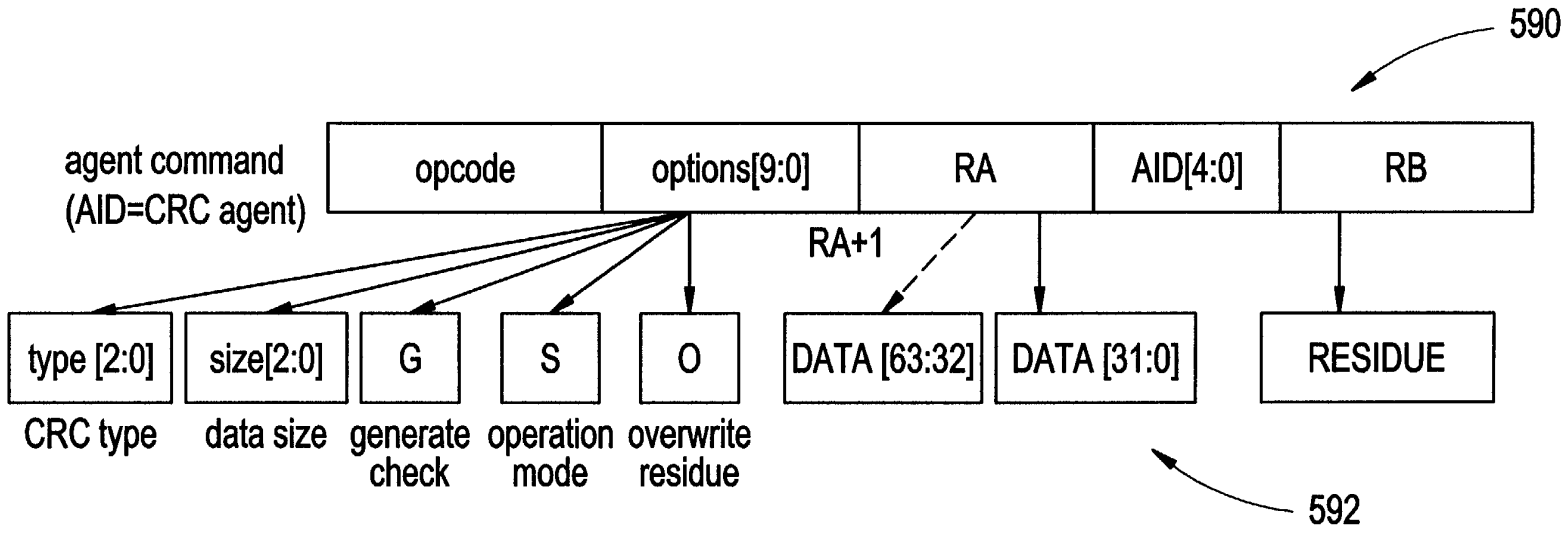
FIG. 55



20m260 "9E5h900T

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FIG. 56



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FIG. 57

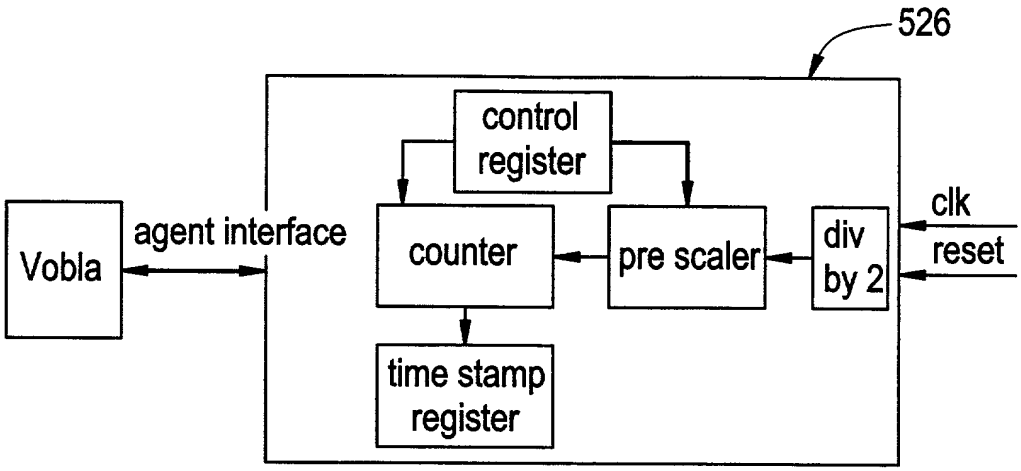
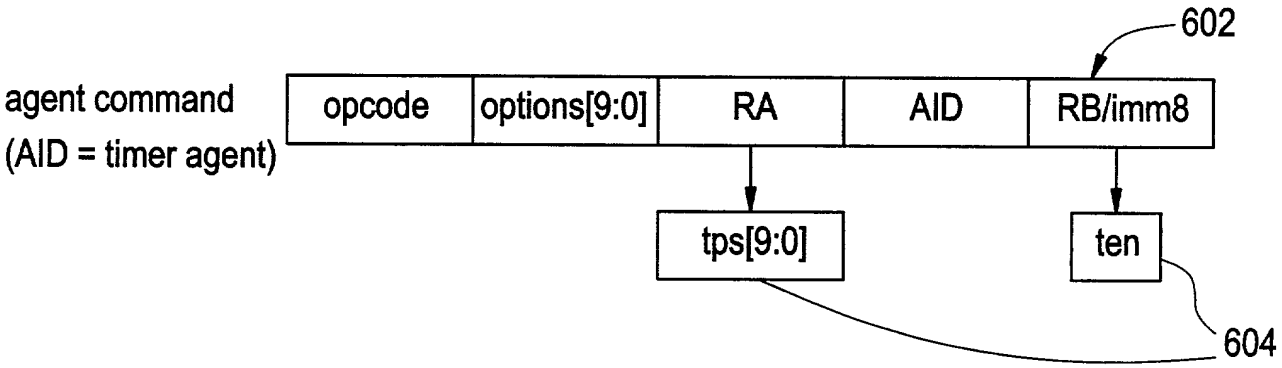


FIG. 58



20h260" 9E5h900F

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FIG. 59

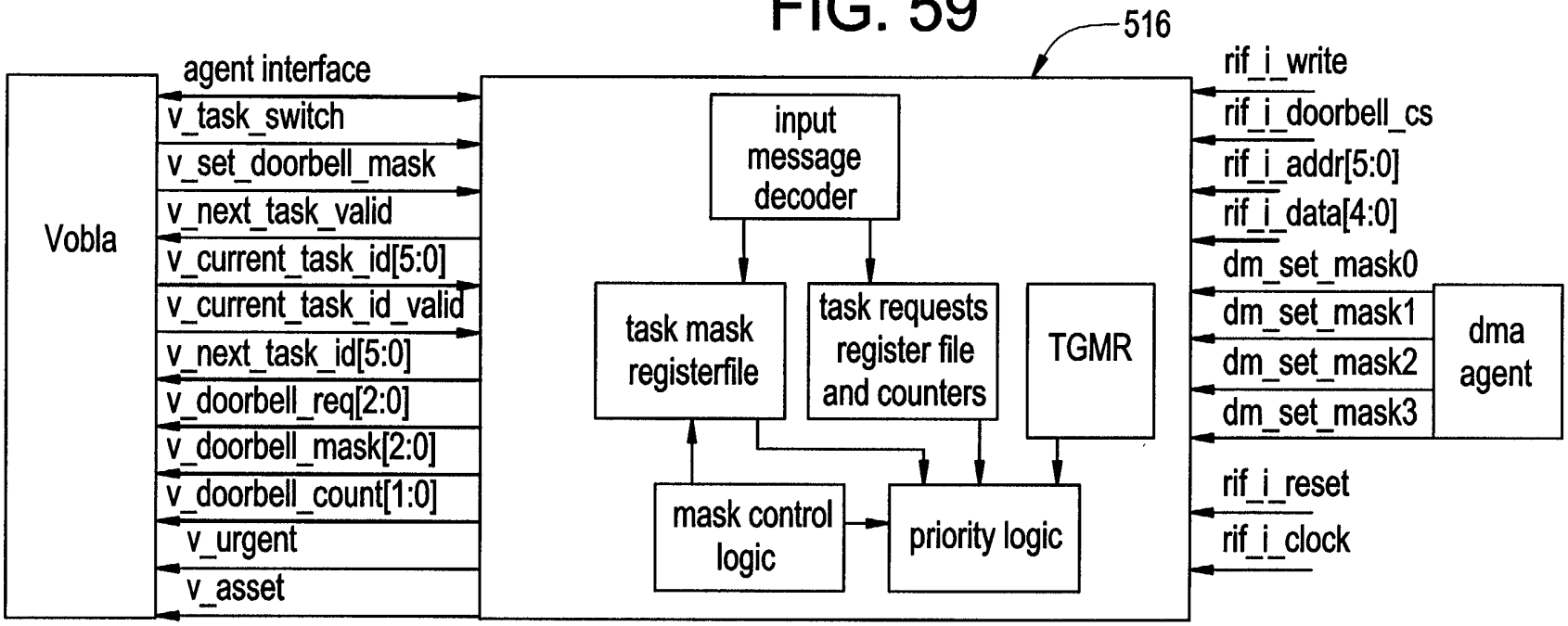
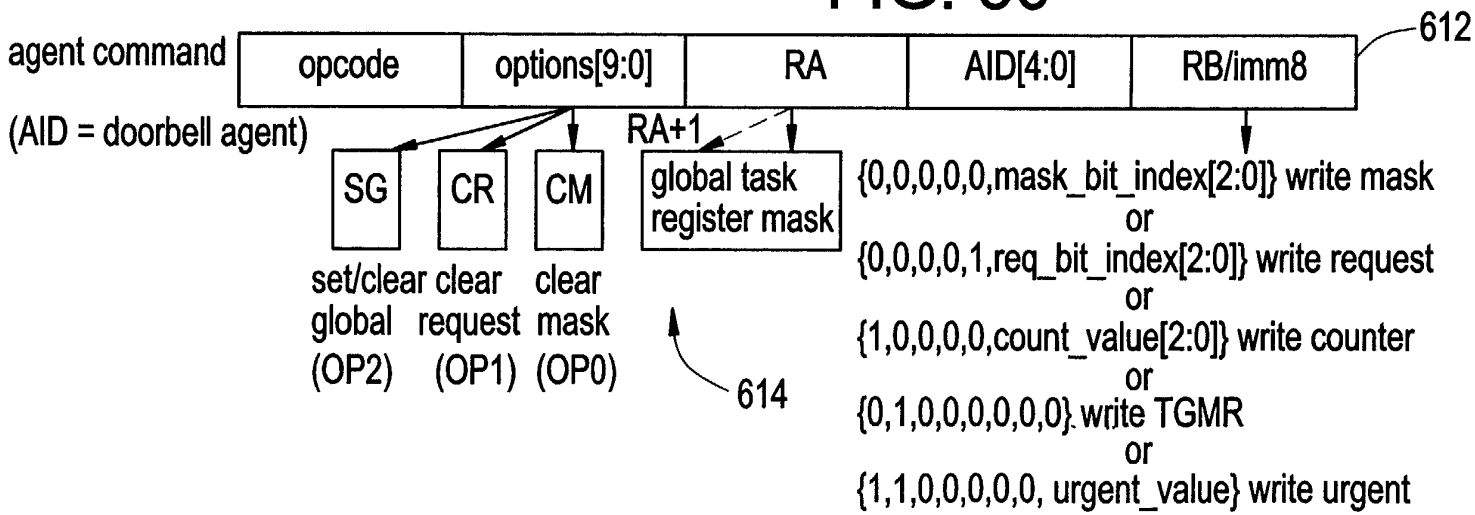
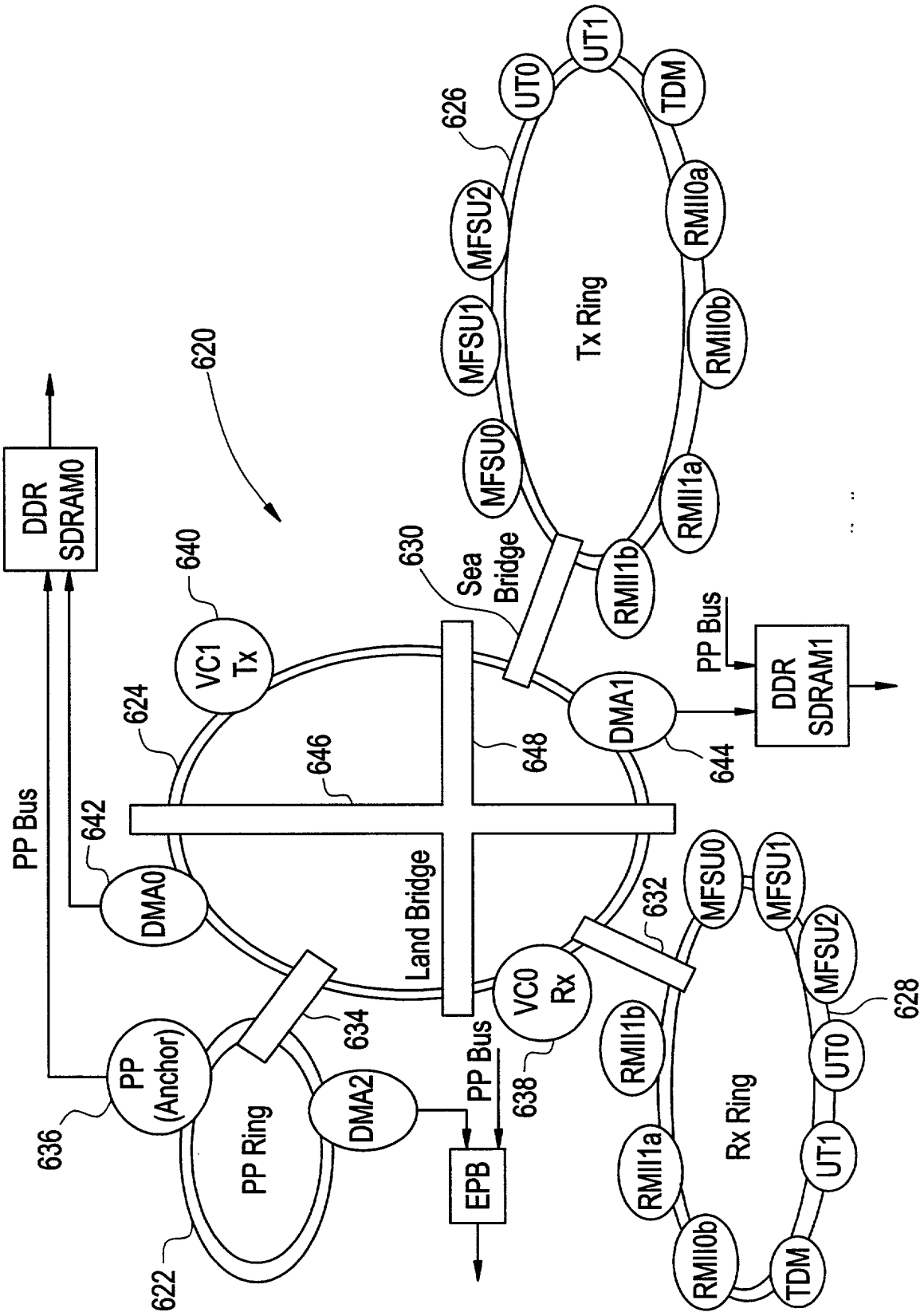


FIG. 60



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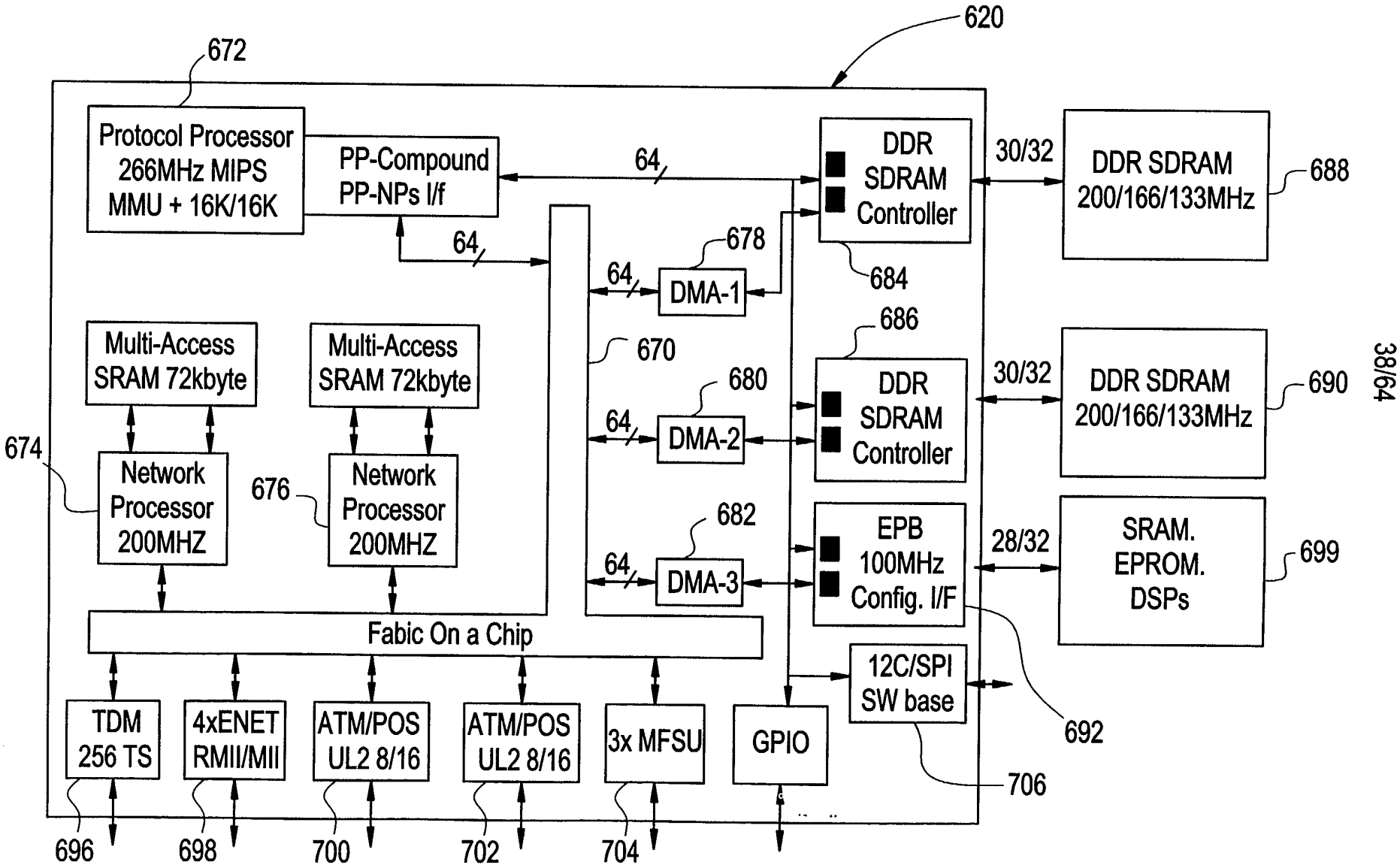
FIG. 61



20h260' 9E6h900T

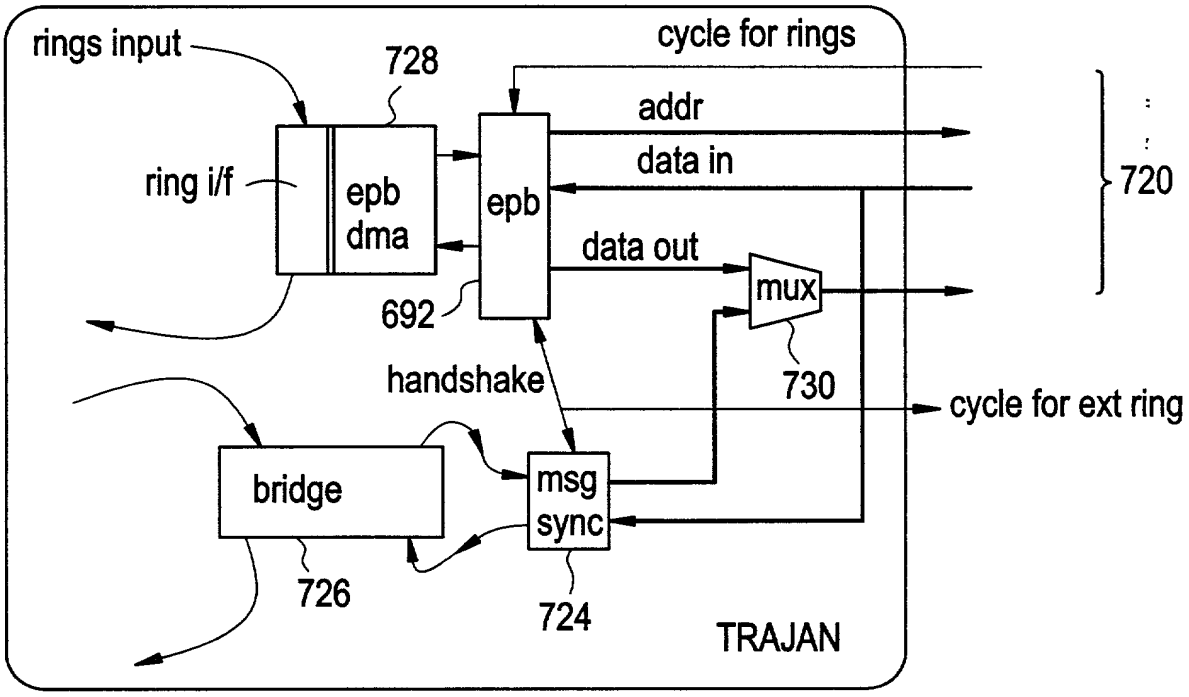
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FIG. 62



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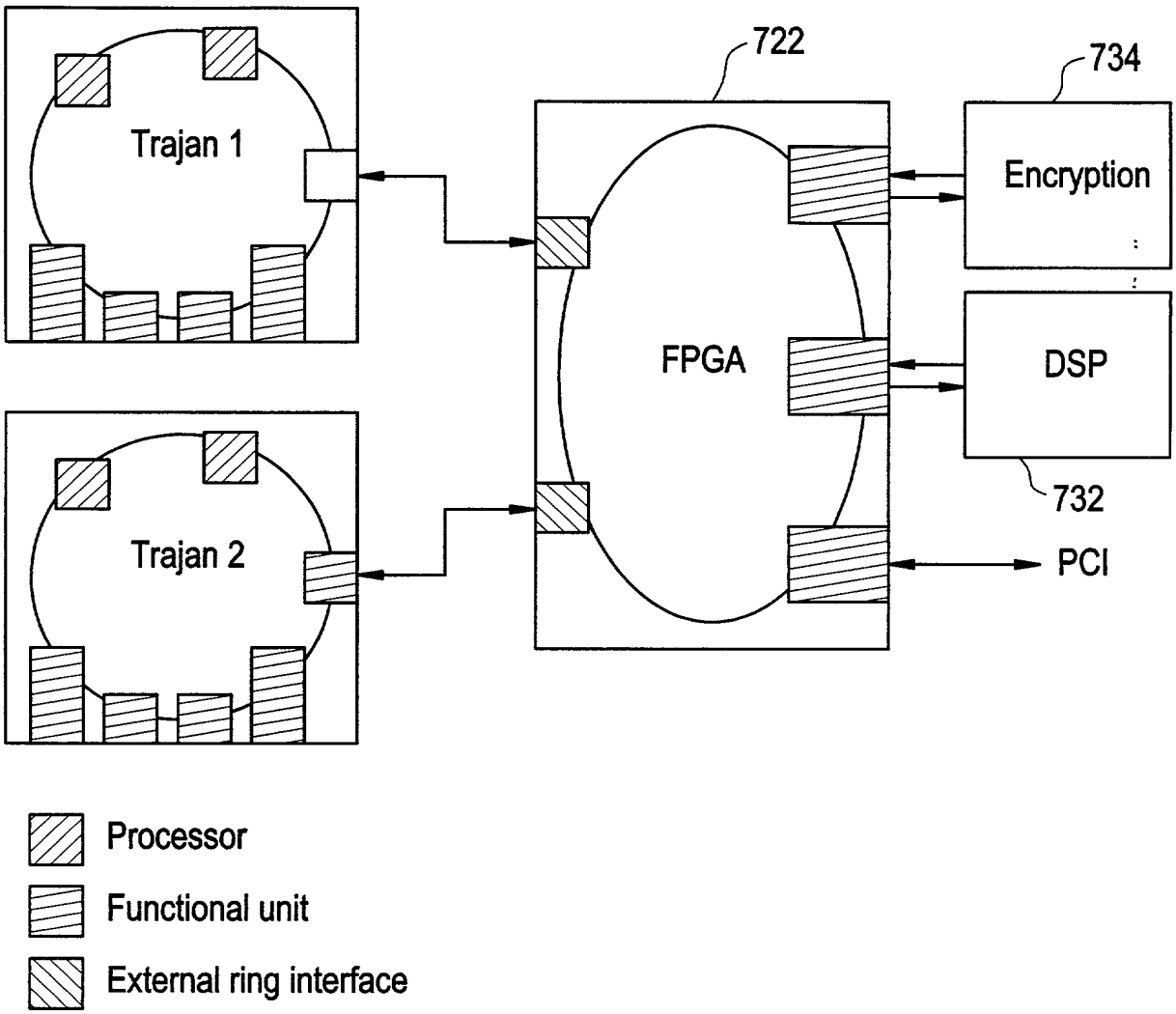
FIG. 63



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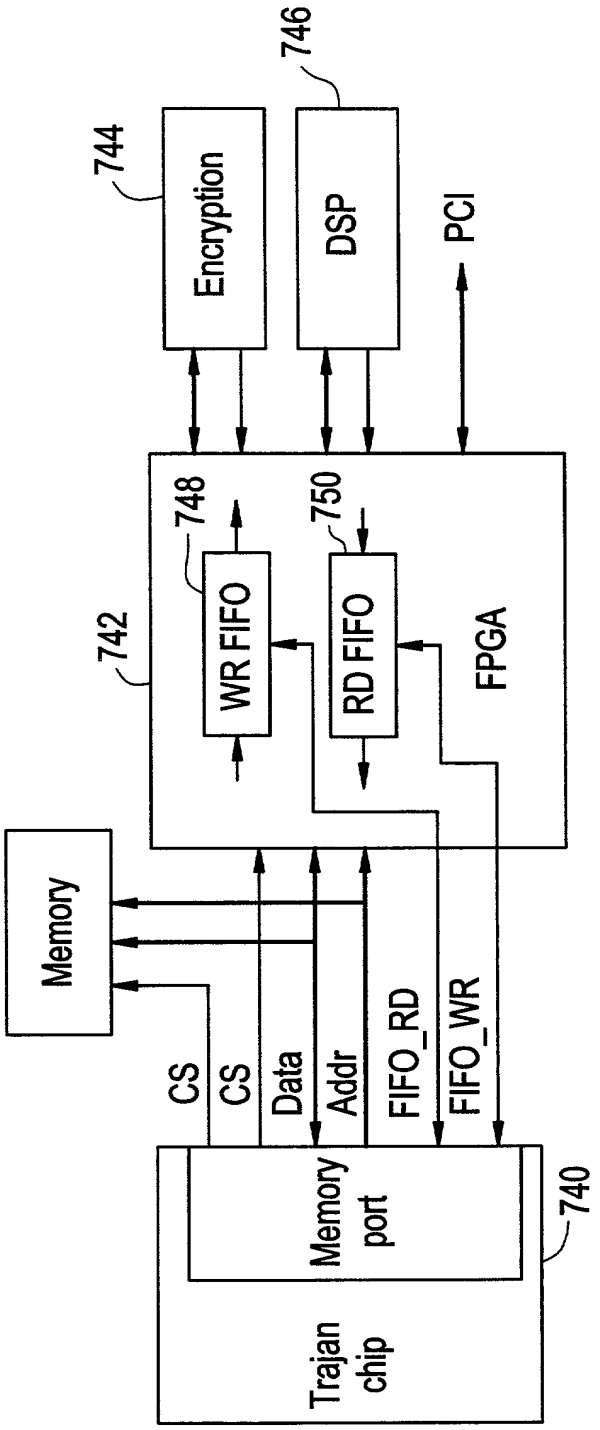
FIG. 64



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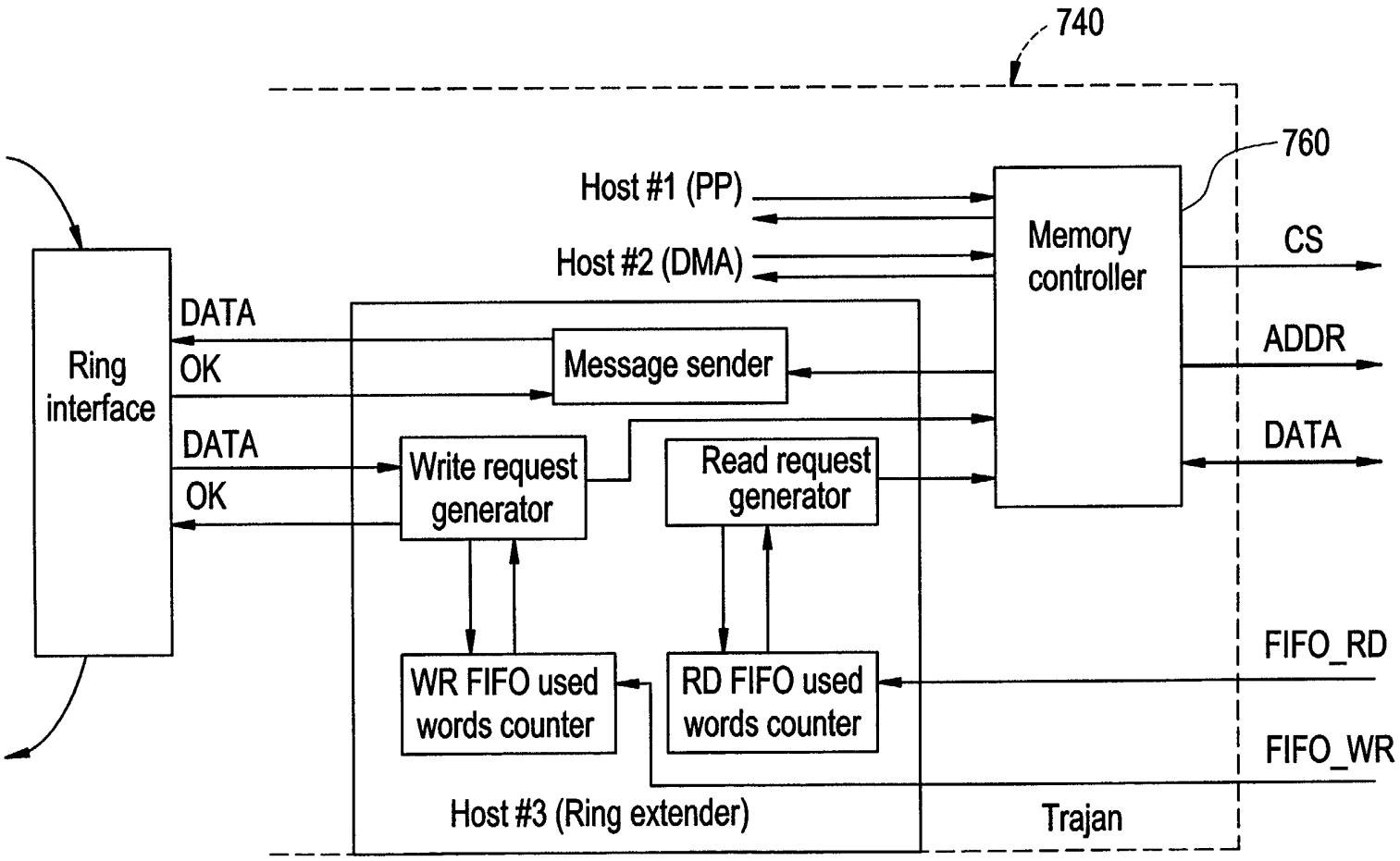
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FIG. 65



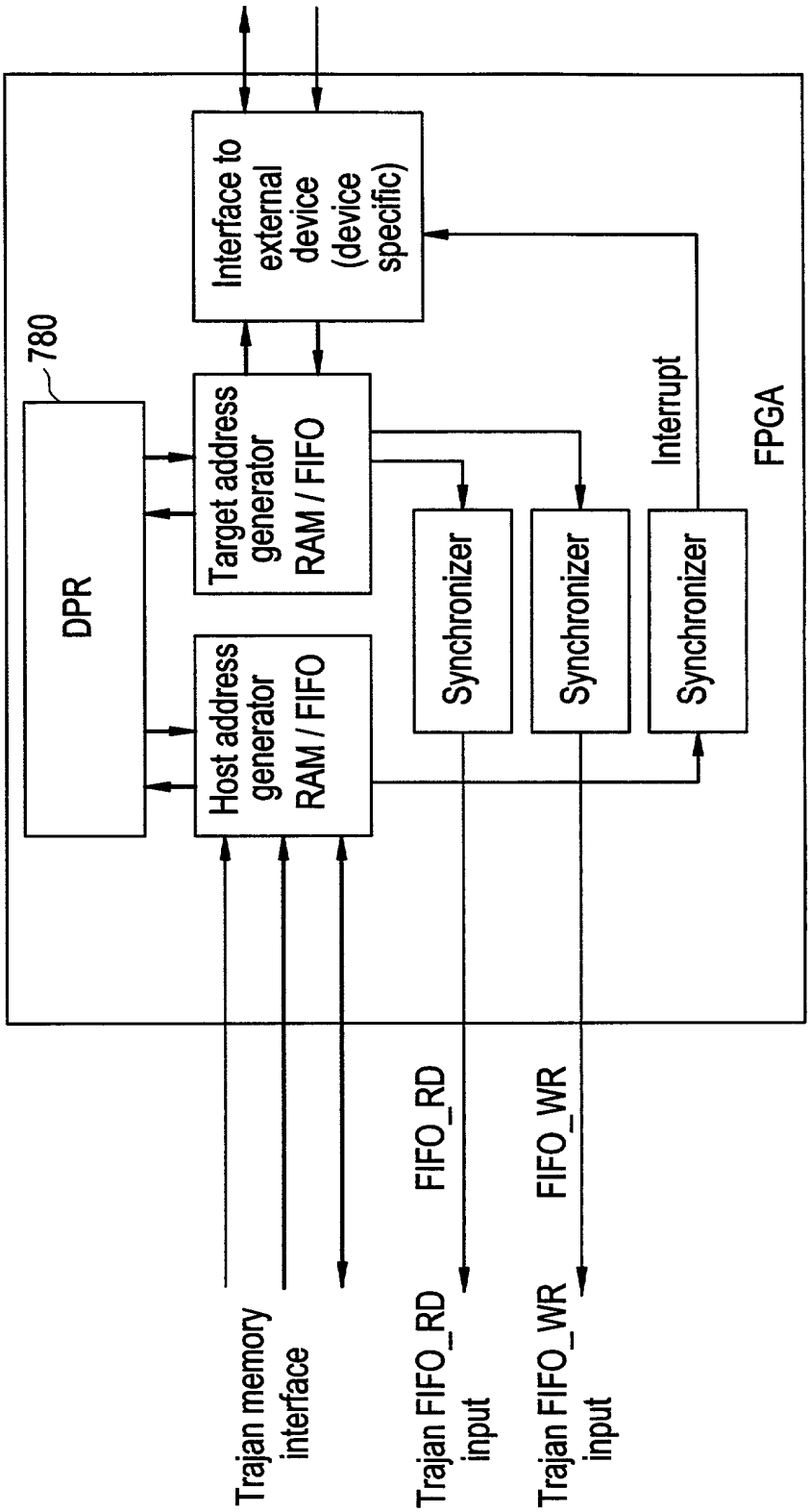
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FIG. 66



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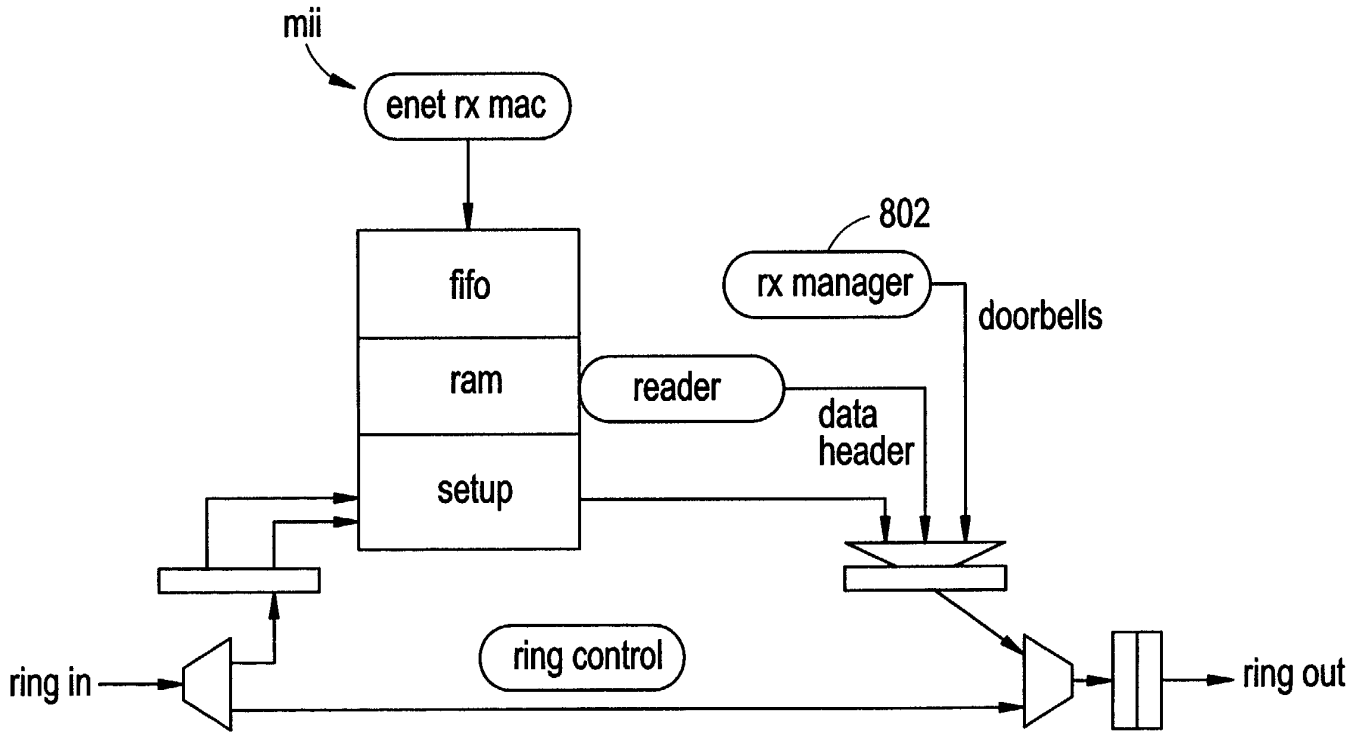
FIG. 67



20h260 9Ech90Ct

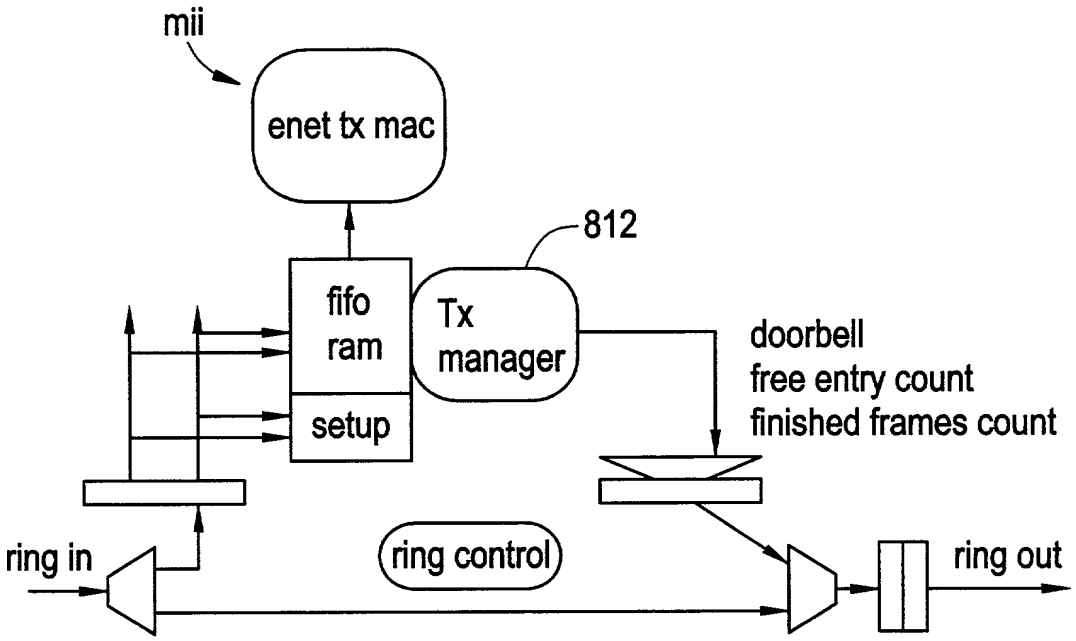
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FIG. 68



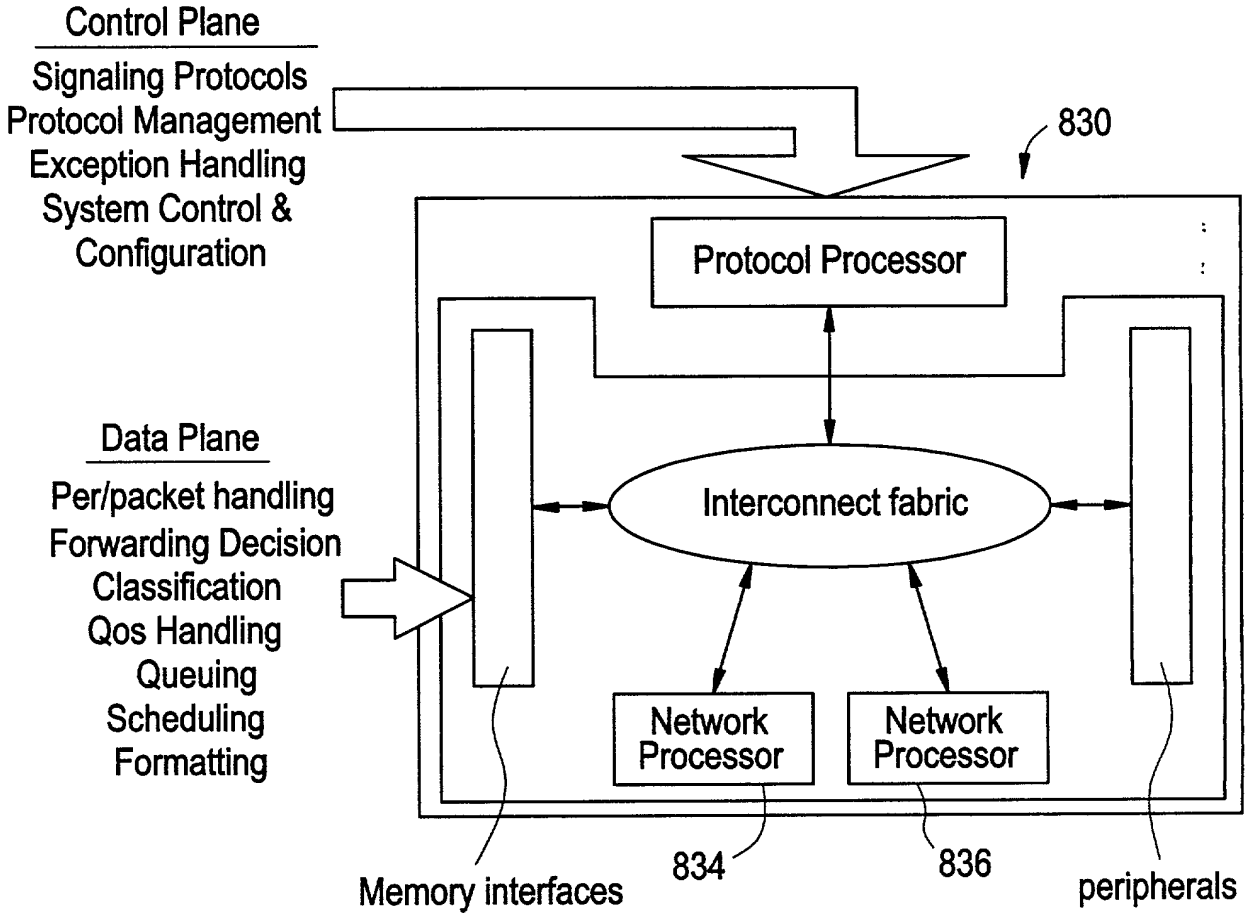
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FIG. 69



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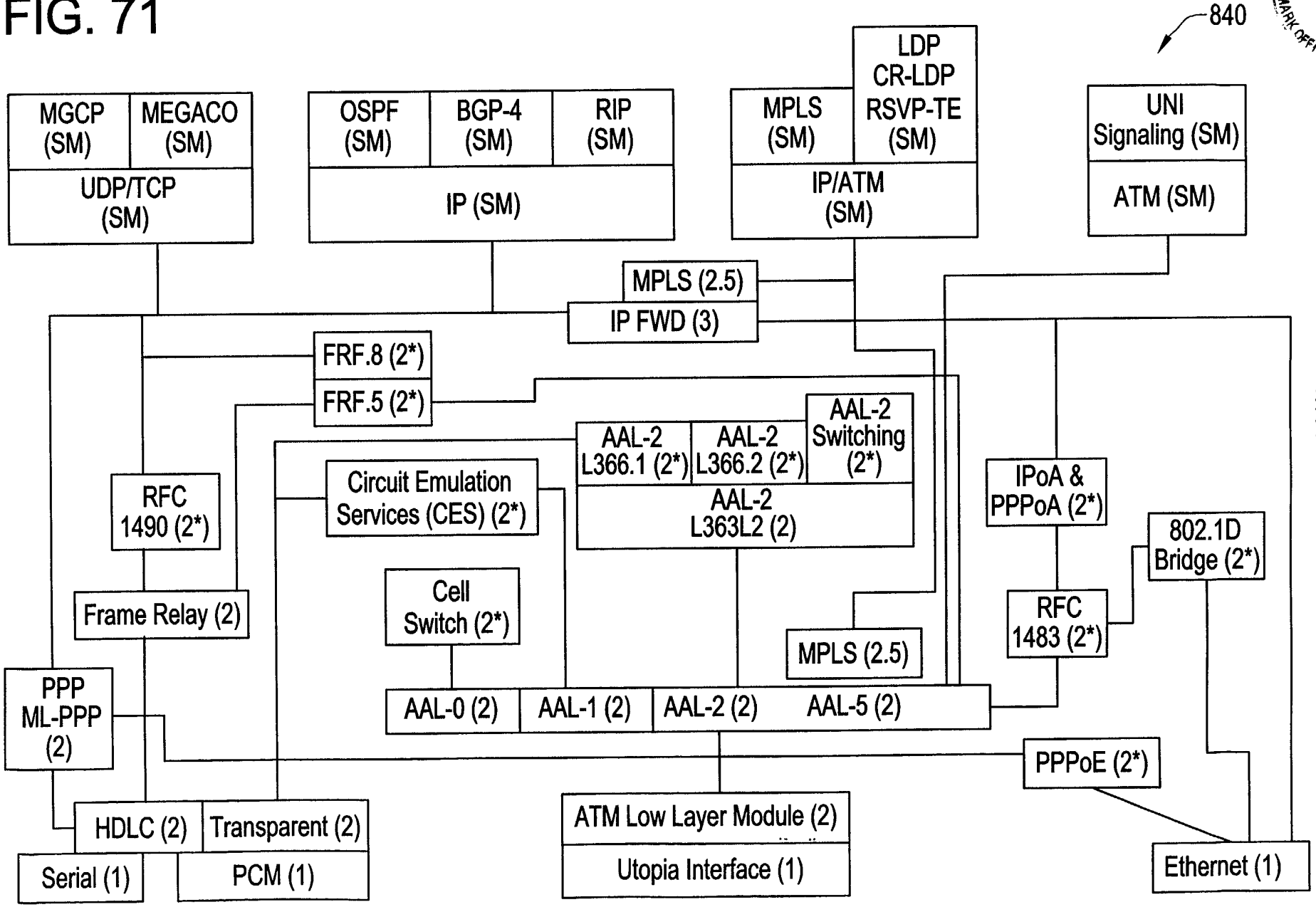
FIG. 70



30h360" 9Ec:h90Ct

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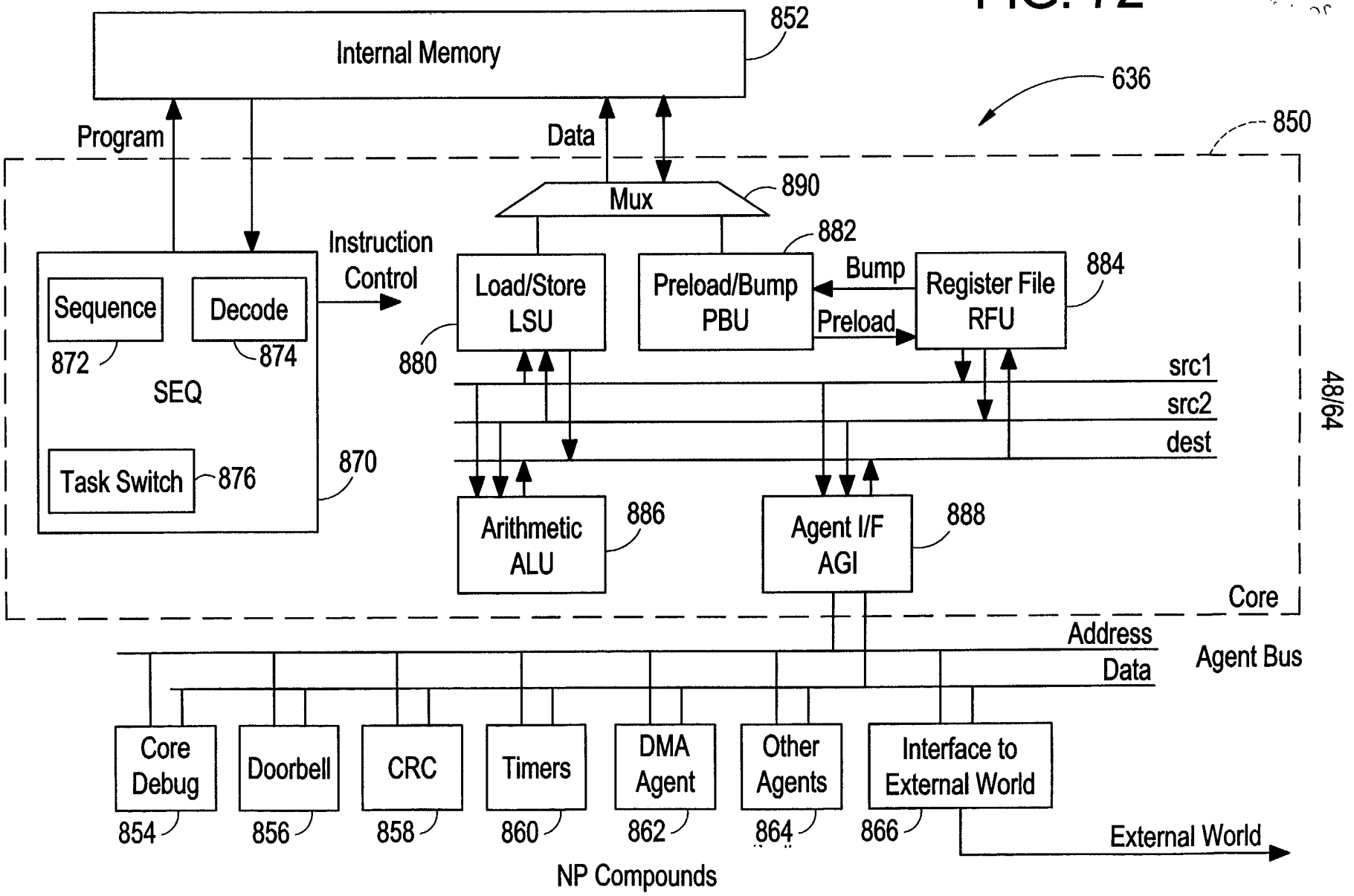
FIG. 71



20h260' 9Ec9h90Ot

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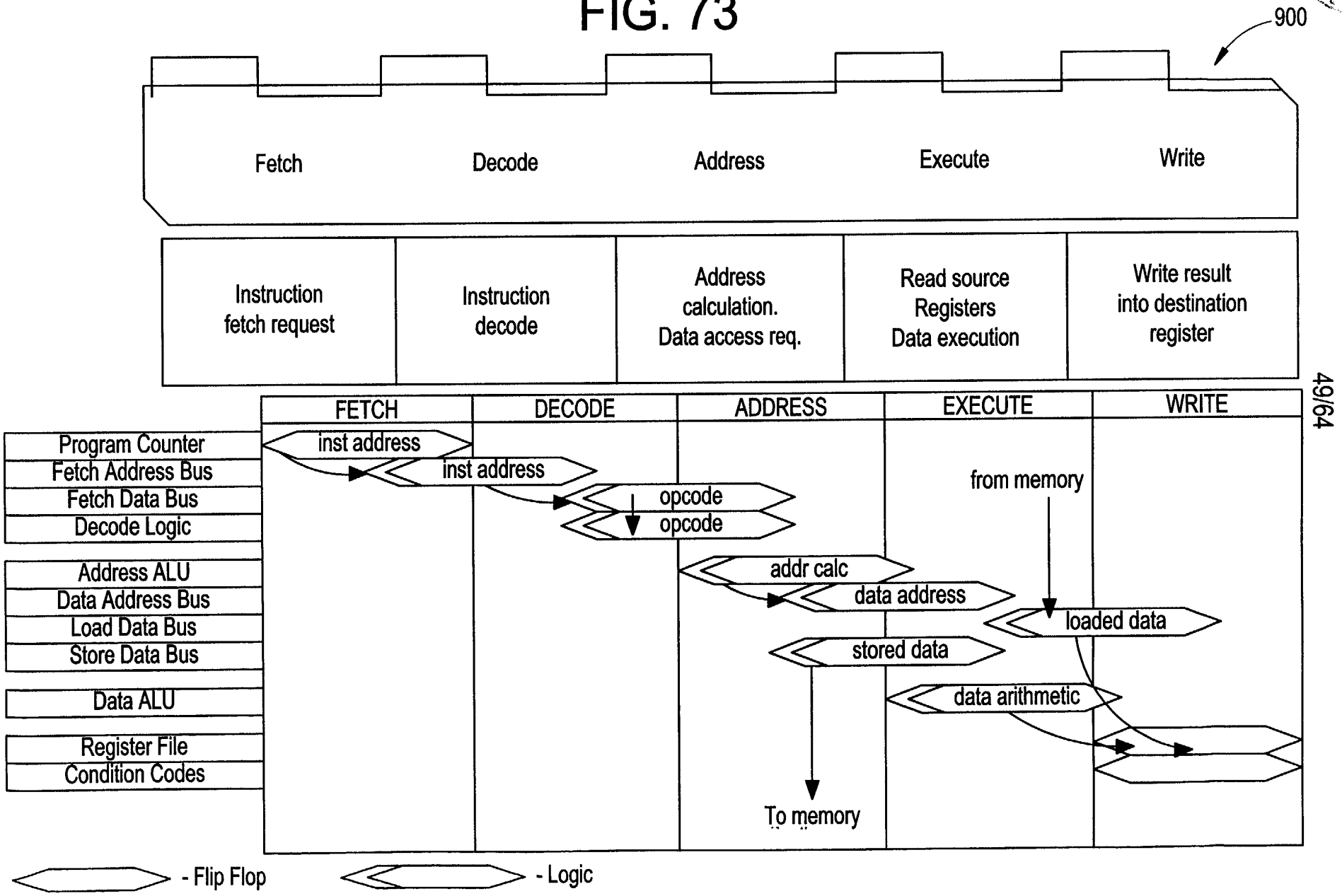
FIG. 72



30h360 95ch900f

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FIG. 73



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FIG. 74

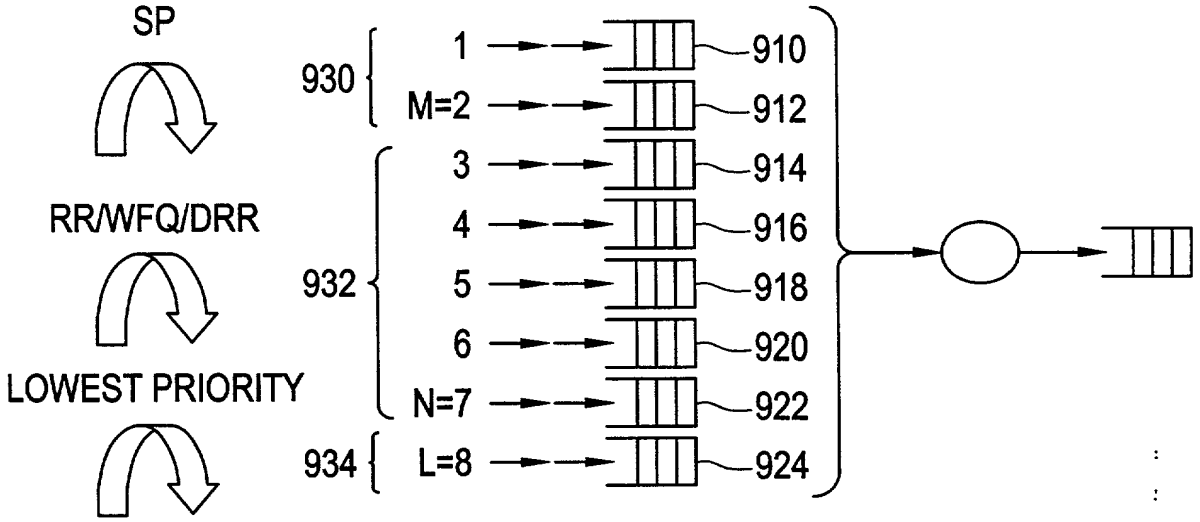
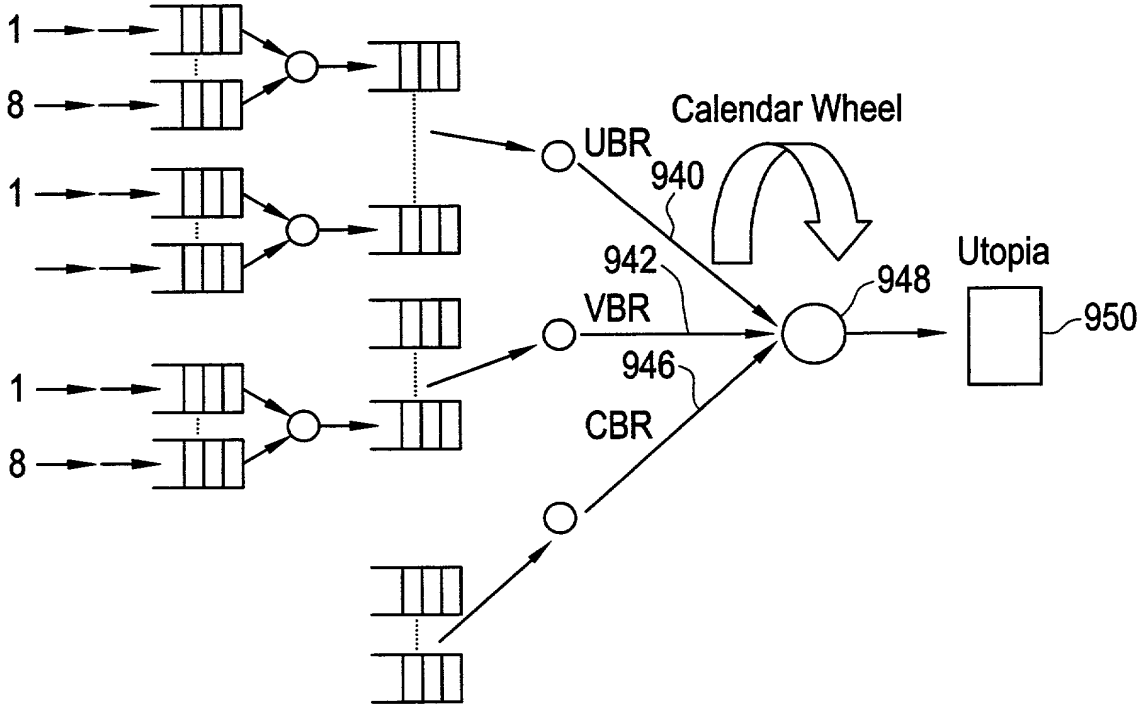


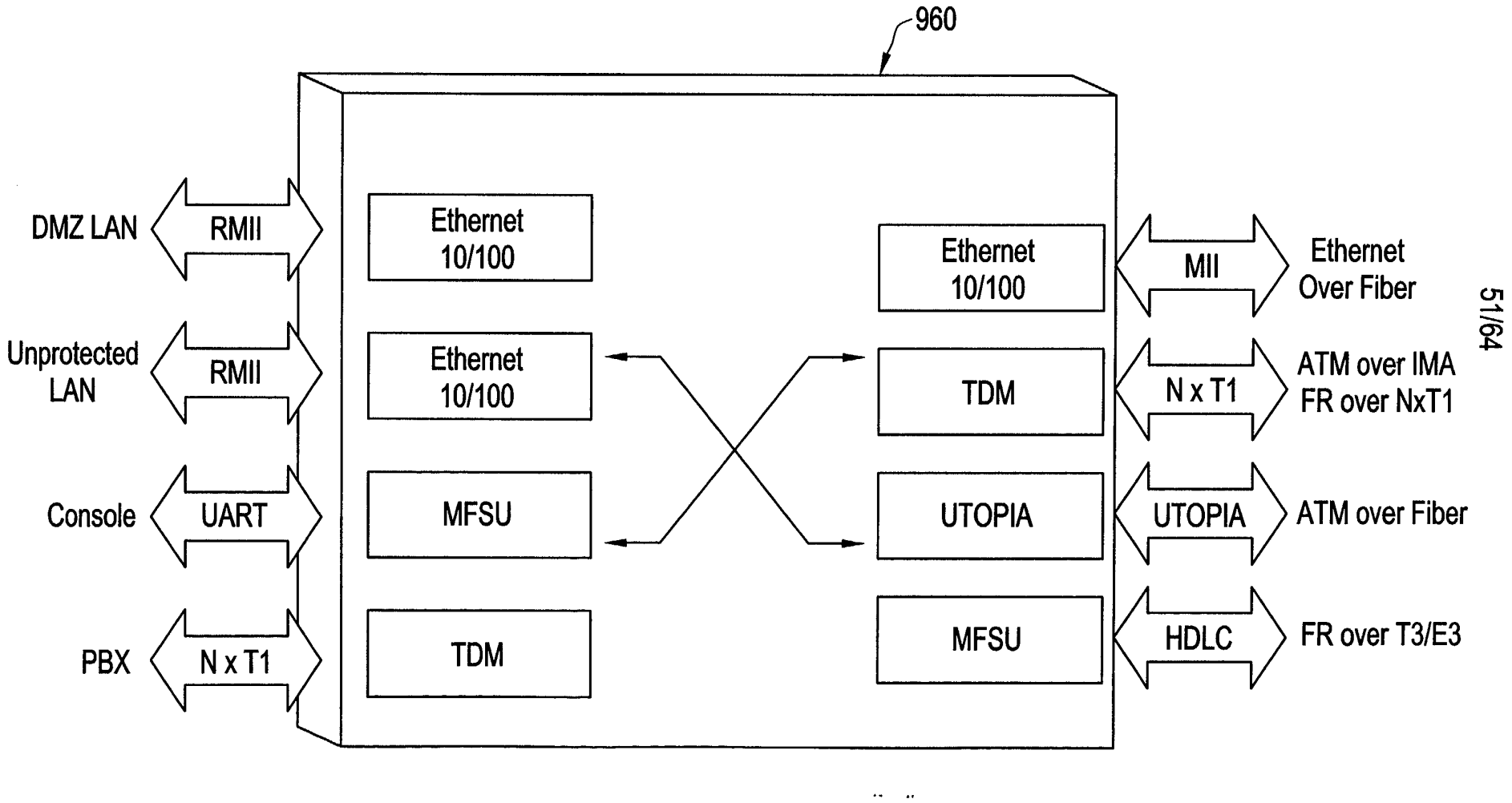
FIG. 75



20h260" 9EC+9001

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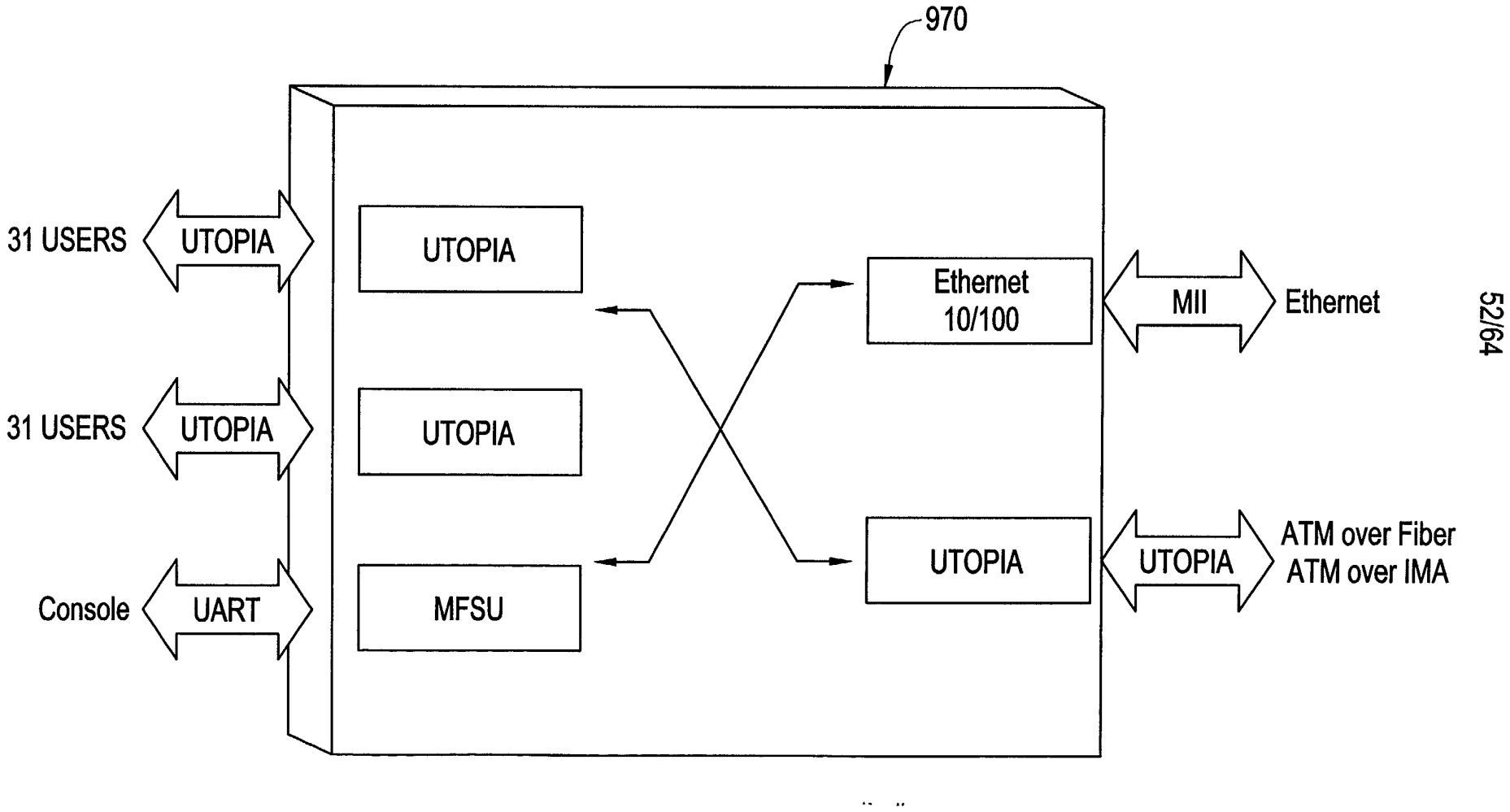
FIG. 76



20h260" 9ECh90DT

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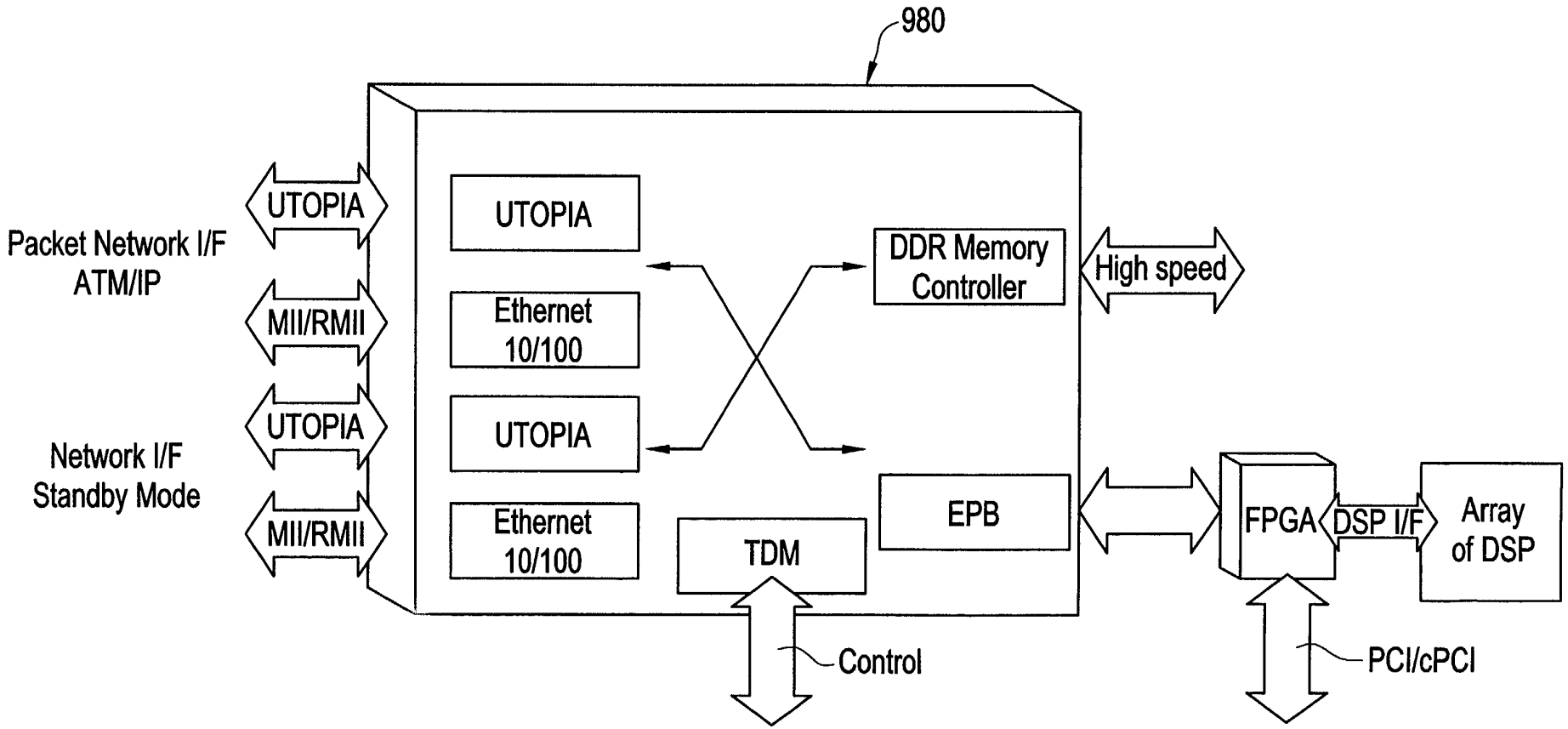
FIG. 77



30m260 96Ch90Df

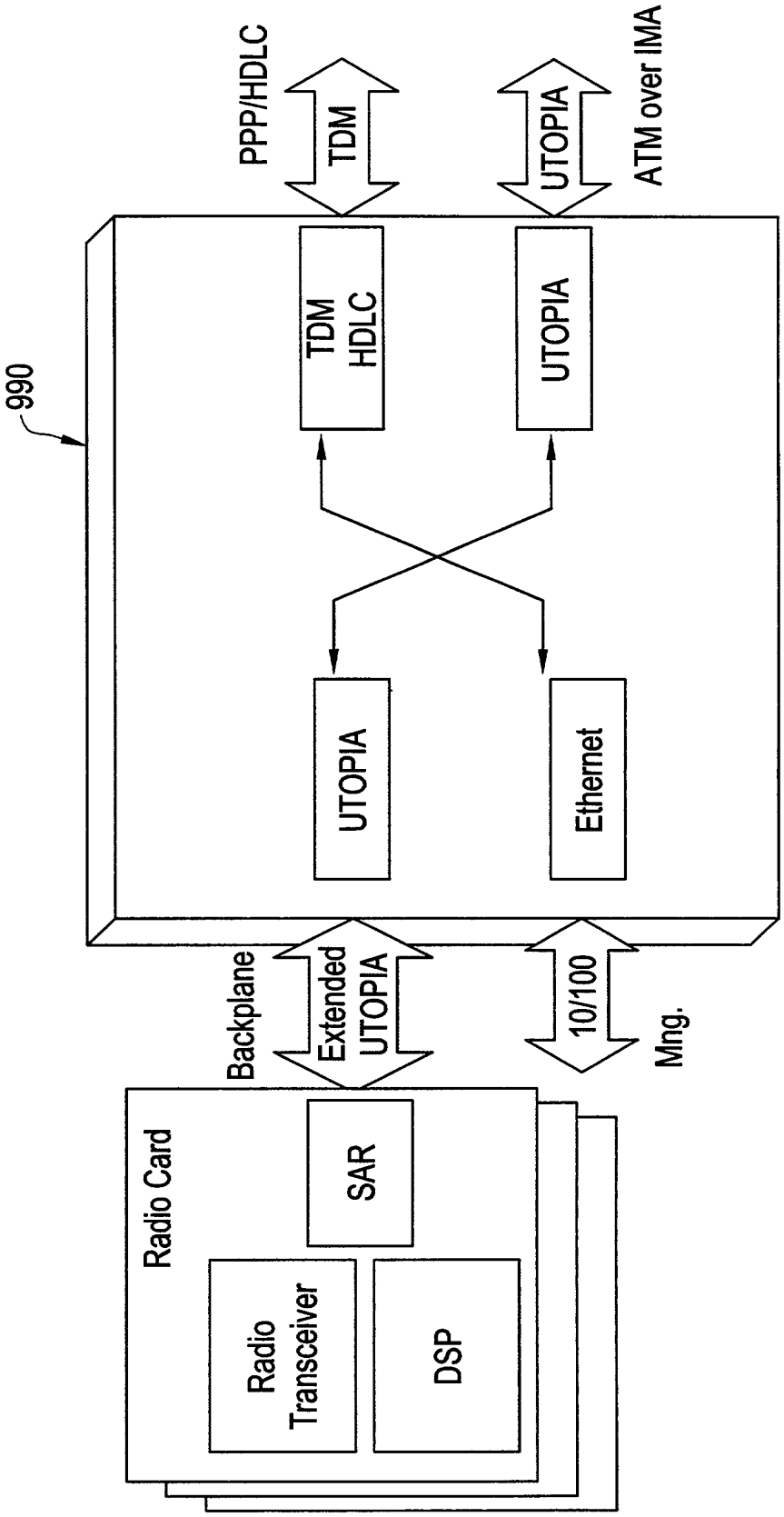
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FIG. 78



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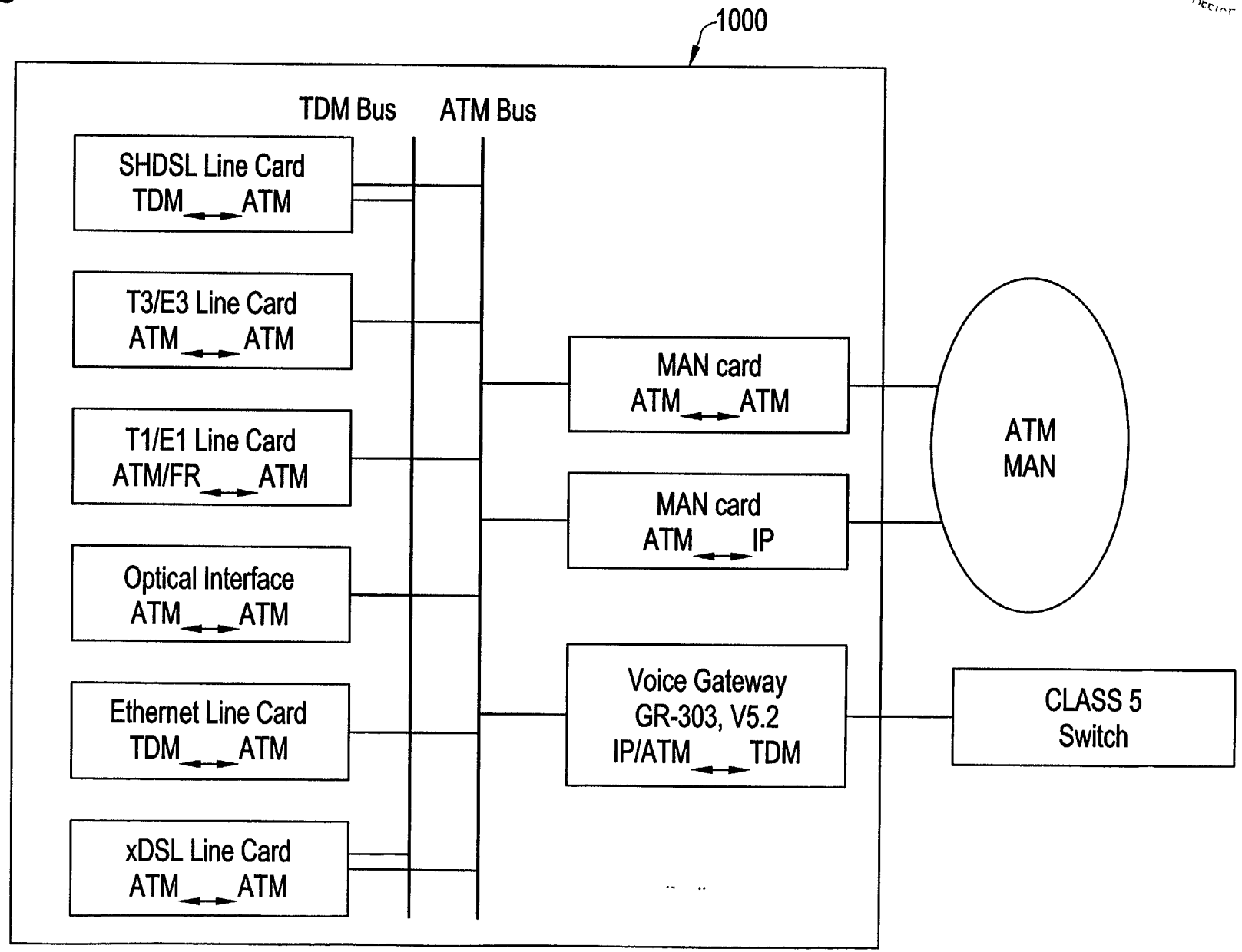
FIG. 79



20h260 9ec4900f

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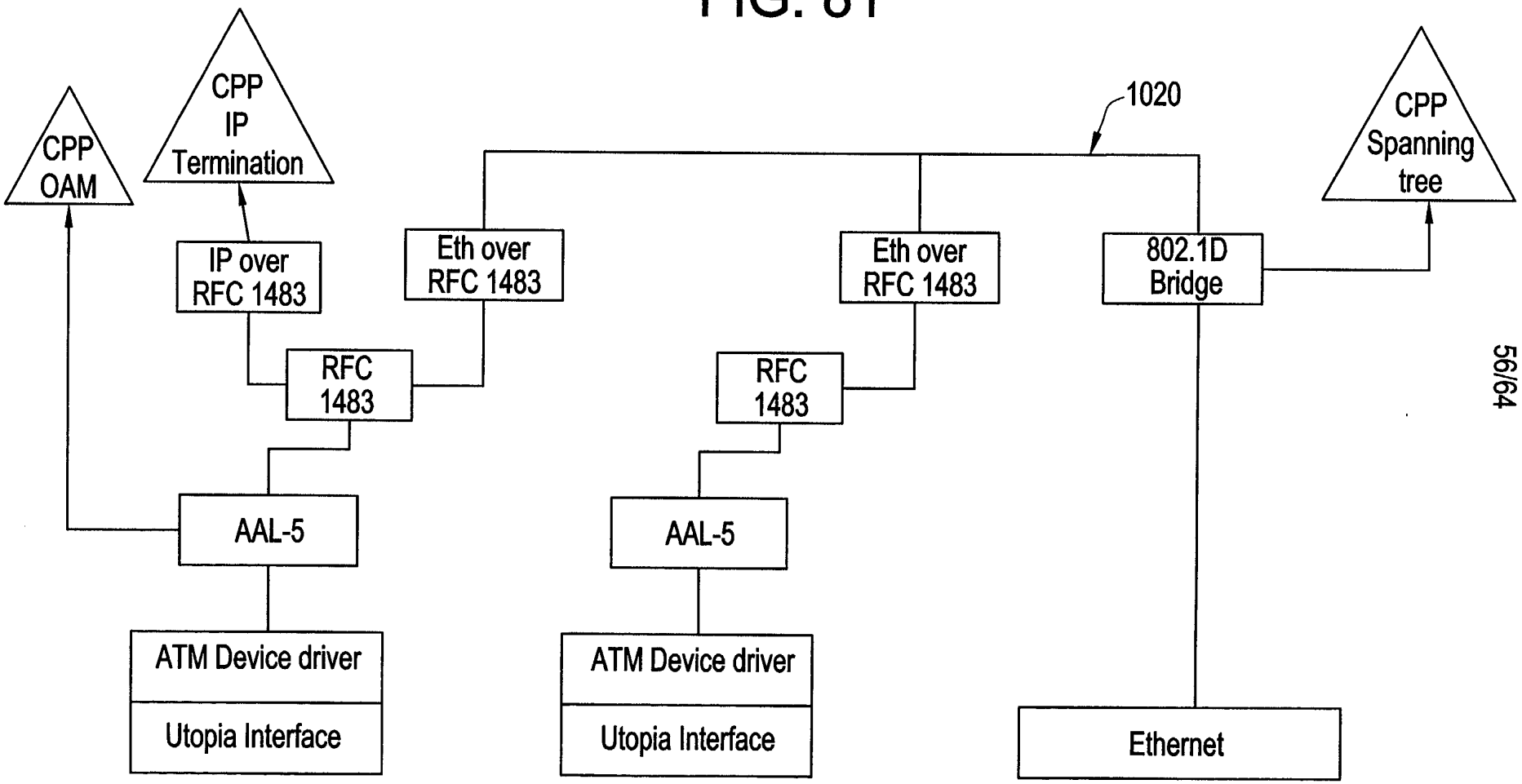
FIG. 80



20th360° 9E6h90DF

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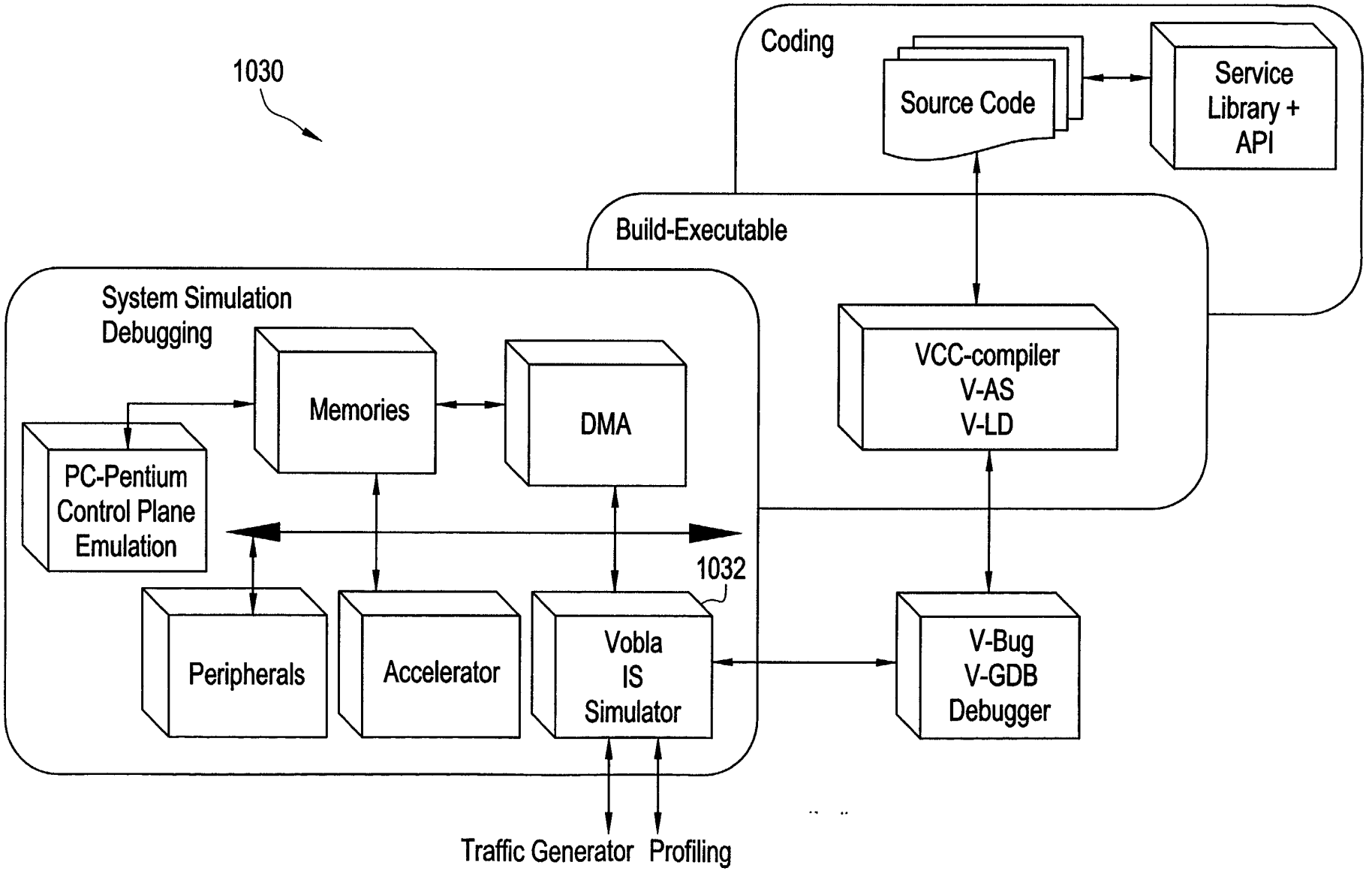
FIG. 81



20h260" 9Ec4h90Ot

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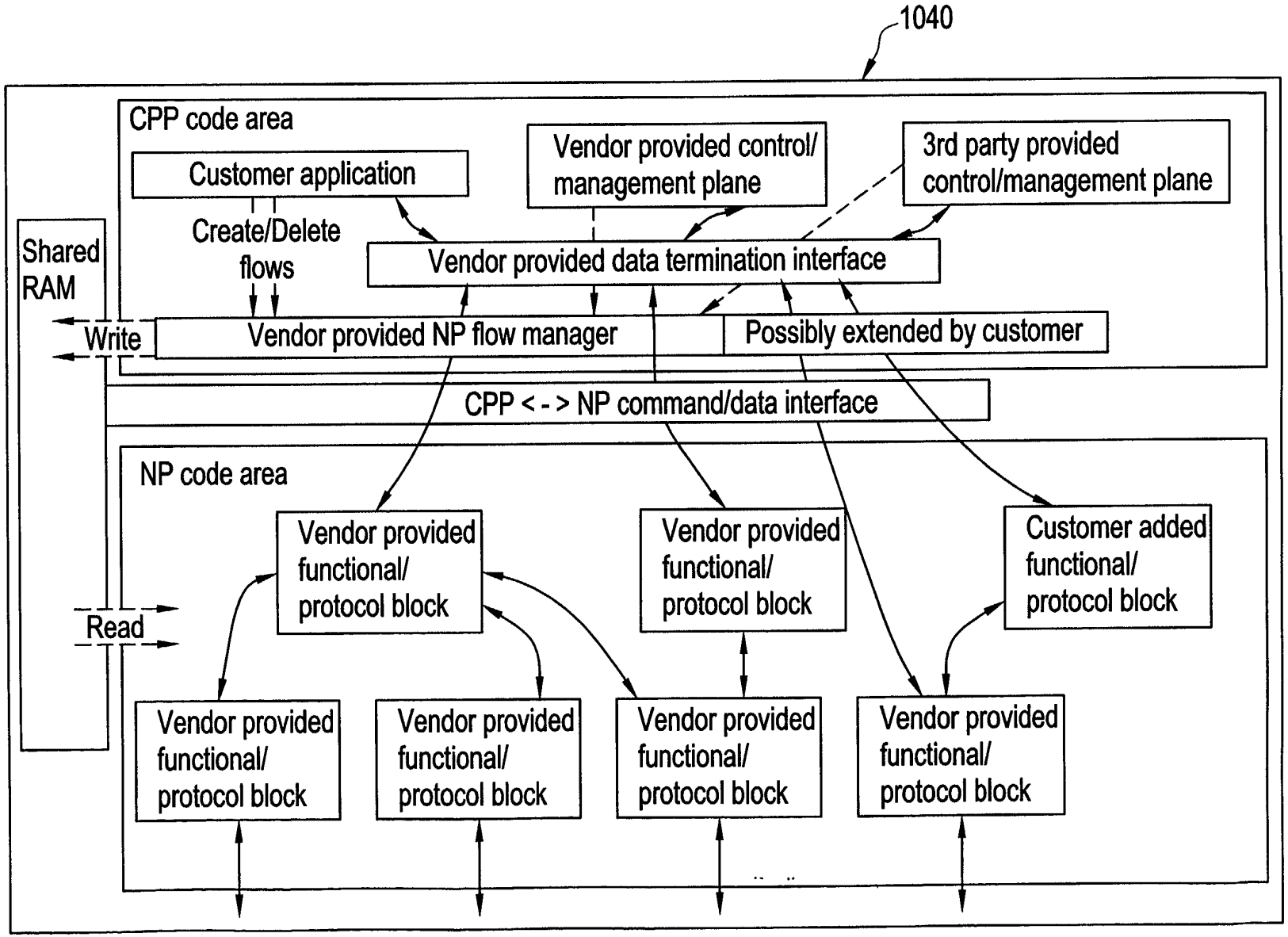
FIG. 82



20h260" 9Ech900T

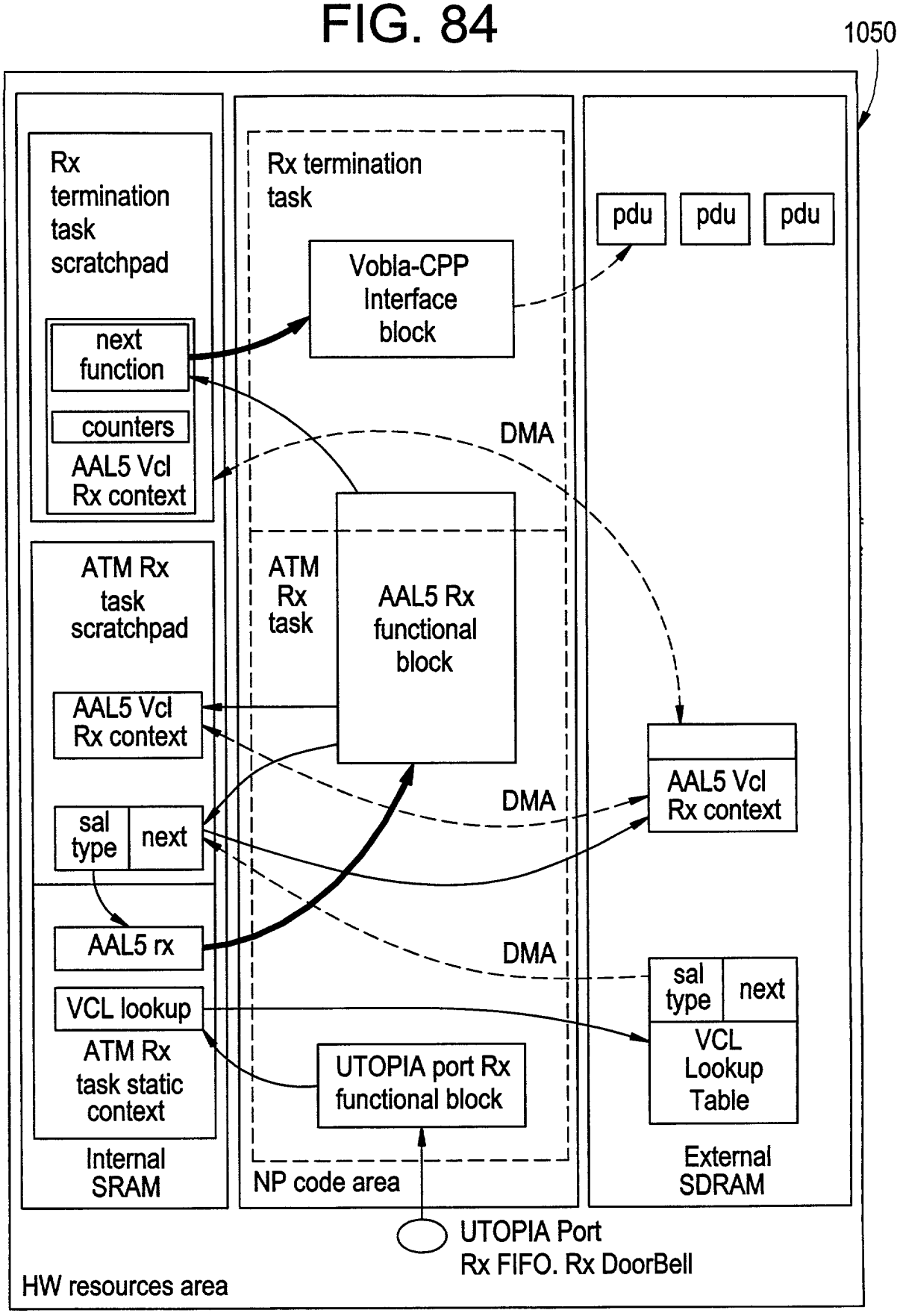
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FIG. 83



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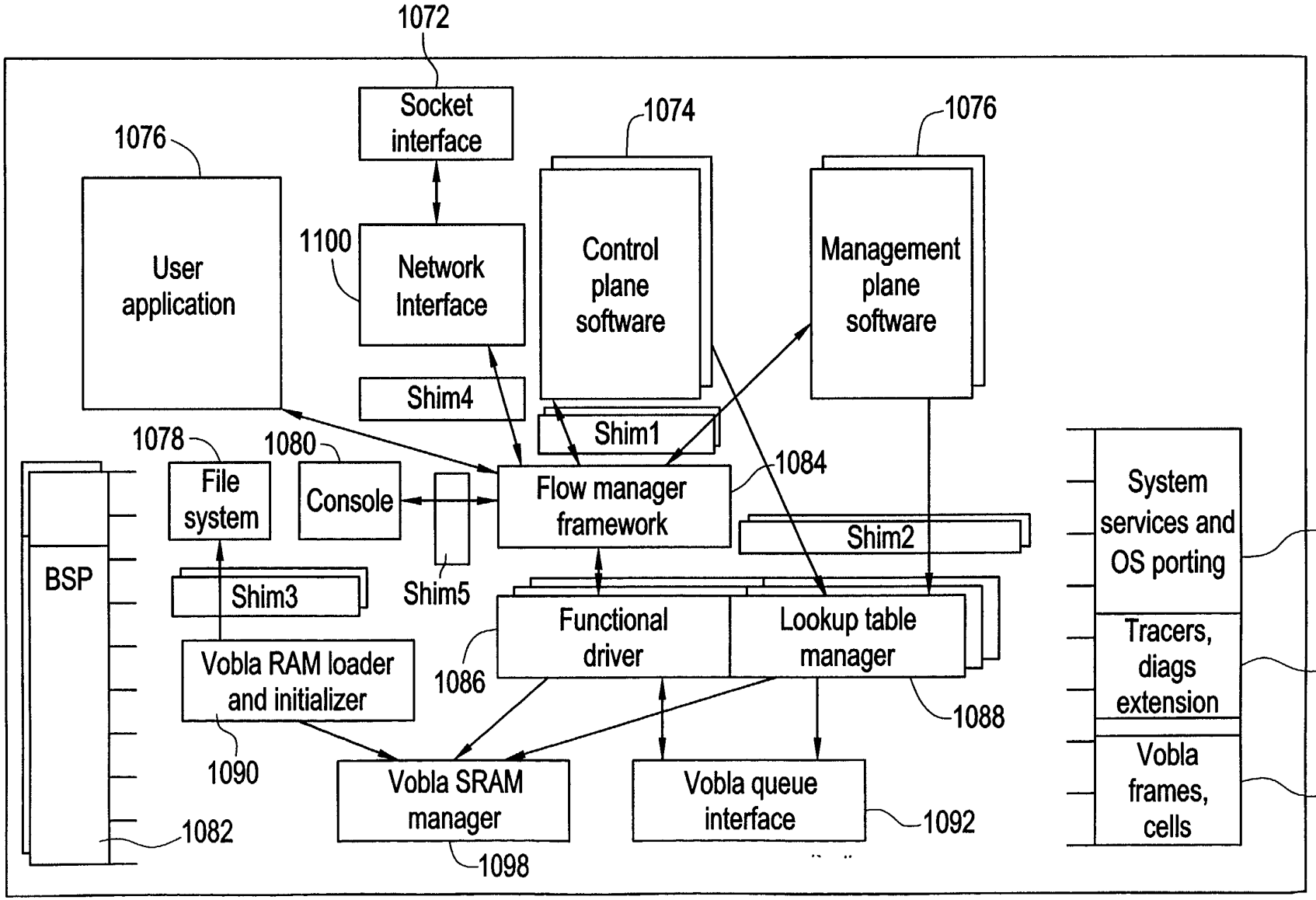
FIG. 84



20h260' 9EEh90DF

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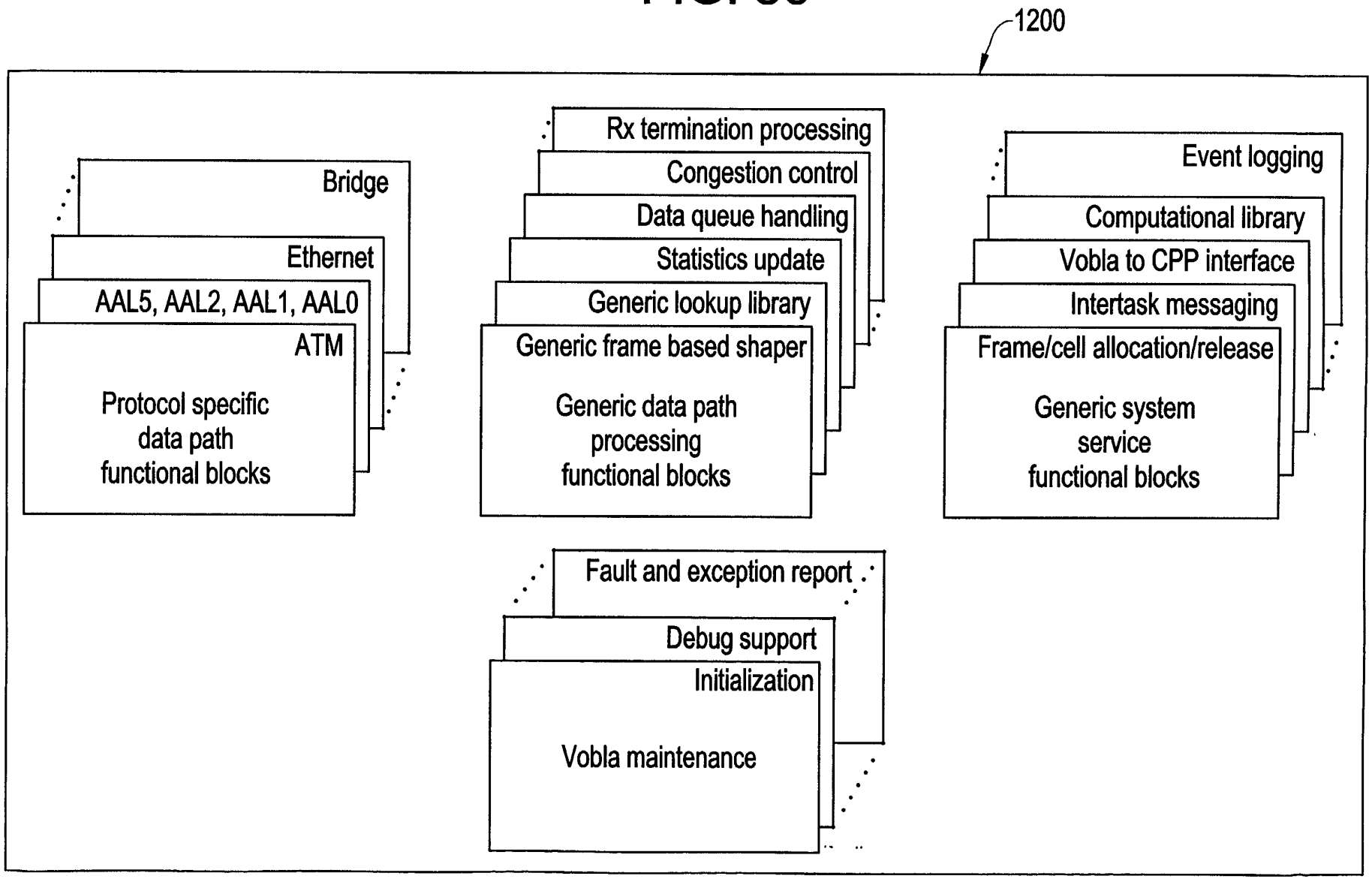
FIG. 85



20h260" 9Ec4h90Qf

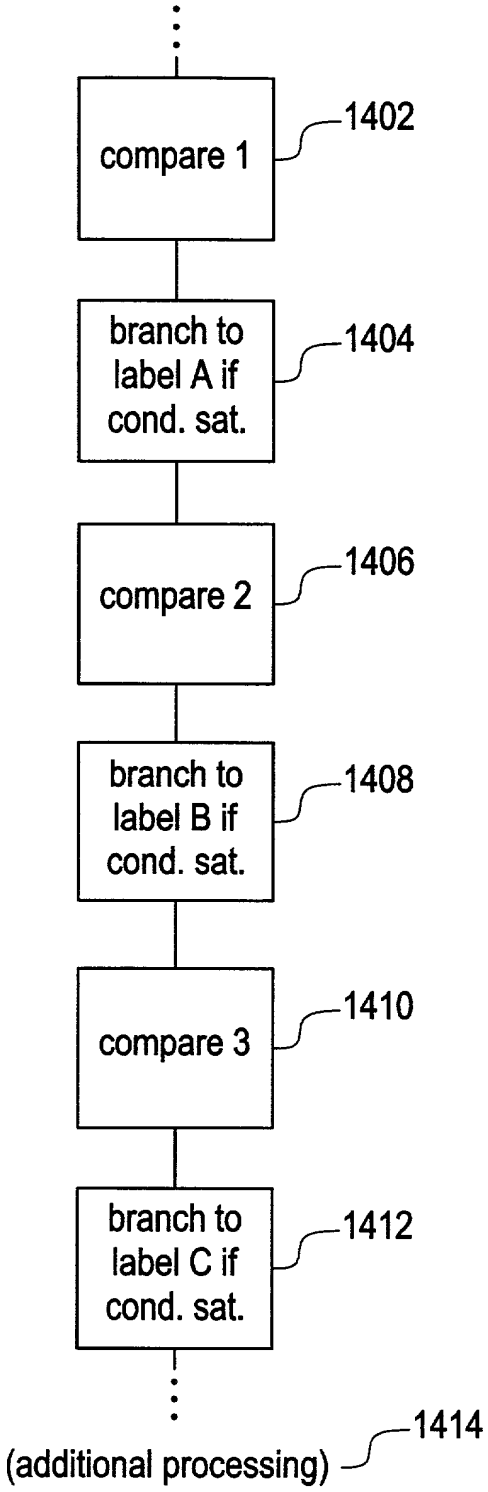
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FIG. 86



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FIG. 87
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FIG. 88

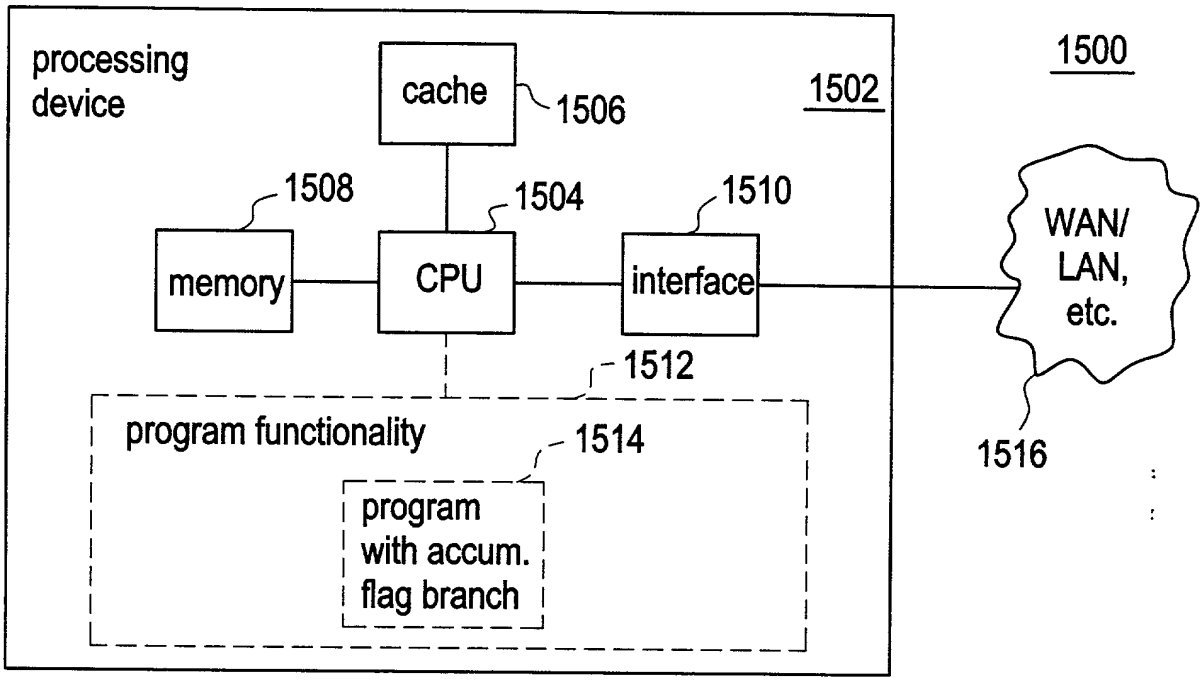
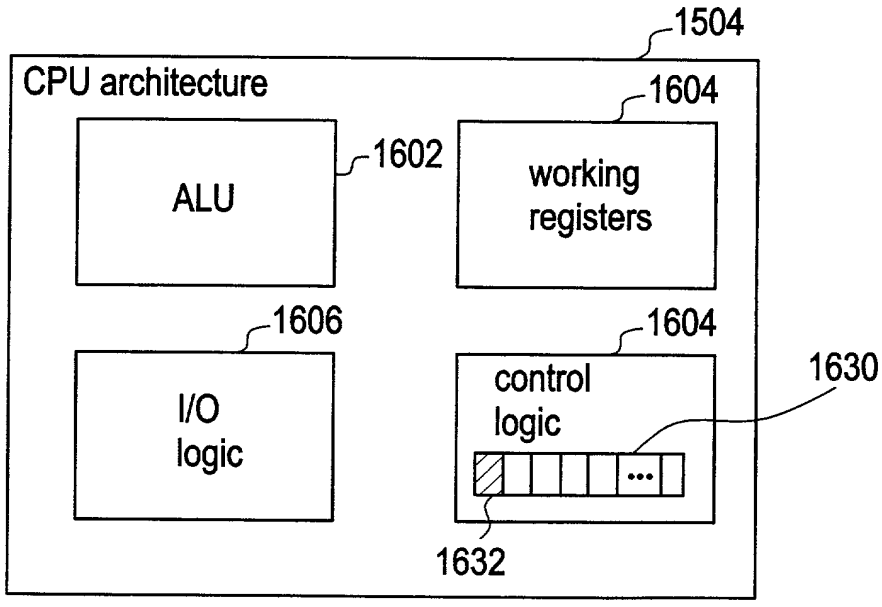


FIG. 89



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FIG. 90

